

## Rigol DG4000 Series Waveform Generator Specifications

All the specifications can be guaranteed if the following two conditions are met unless where noted.

- The generator is within the calibration period and has performed self-calibration.
- The generator has been working continuously for at least 30 minutes under the specified temperature (18°C ~ 28°C).

All the specifications are guaranteed unless those marked with "typical".

Model	DG4162	DG4102	DG4062
Channel	2	2	2
Maximum Frequency	160MHz	100MHz	60 MHz
Sample Rate	500MSa/s		
<b>Waveforms</b>			
Standard waveforms	Sine, Square, Ramp, Pulse, Noise, Harmonics		
Arbitrary Waveforms	150 kinds, including Sinc, Exponential Rise, Exponential Fall, ECG, Gauss, HaverSine, Lorentz, Dual-Tone, DC, etc.		
<b>Frequency Characteristics</b>			
Sine	1μHz to 160MHz	1μHz to 100MHz	1μHz to 60MHz
Square	1μHz to 50MHz	1μHz to 40MHz	1μHz to 25MHz
Ramp	1μHz to 4MHz	1μHz to 3MHz	1μHz to 1MHz
Pulse	1μHz to 40MHz	1μHz to 25MHz	1μHz to 15MHz
Harmonic	1uHz to 80MHz	1uHz to 50MHz	1uHz to 30MHz
Noise (-3dB)	120MHz bandwidth	80MHz bandwidth	60MHz bandwidth
Arbitrary Waveform	1μHz to 40MHz	1μHz to 25MHz	1μHz to 15MHz
Resolution	1μHz		
Accuracy	±2ppm, 18°C to 28°C		
<b>Sine Wave Spectrum Purity</b>			
Harmonic Distortion	Typical (0dBm) DC-1MHz: <-60dBc		

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	1MHz-10MHz: <-55dBc 10MHz-100MHz: <-50dBc 100MHz-160MHz: <-40dBc		
Total Harmonic Distortion	<0.1% (10Hz-20kHz,0dBm)		
Spurious (non-harmonic)	Typical (0dBm) ≤10MHz <-65dBc >10MHz <-65dBc+6dB/octave		
Phase Noise	Typical (0dBm, 10kHz deviation) 10MHz: ≤-115dBc/Hz		
<b>Signal Characteristics</b>			
<b>Square</b>			
Rise/Fall Time	Typical (1Vpp) <8ns	Typical (1Vpp) <10ns	Typical (1Vpp) <12ns
Overshoot	Typical (100KHz, 1Vpp) <3%		
Duty Cycle	≤10MHz: 20.0% to 80.0% 10MHz-40MHz: 40.0% to 60.0% >40MHz: 50.0% (fixed)		
Non-symmetry	1% of period +5ns		
Jitter (rms)	Typical (1MHz, 1Vpp, 50Ω) ≤5MHz 2ppm+500ps > 5MHz 500ps		
<b>Ramp</b>			
Linearity	≤1% of peak output (Typical, 1kHz, 1VPP, 100% Symmetry)		
Symmetry	0% to 100%		
<b>Pulse</b>			
Period	25ns to 1000000s	40 ns to 1000000 s	66.7 ns to 1000000 s
Pulse Width	≥10ns	≥12ns	≥18ns
Leading/ Trailing Edge Time	≥5ns	≥7ns	≥11ns
Overshoot	Typical (1Vpp) <3%		
Jitter (rms)	Typical (1Vpp) ≤5MHz 2ppm+500ps		

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	> 5MHz 500ps		
<b>Arb</b>			
Waveform Length	16k points		
Vertical Resolution	14bits		
Sample Rate	500MSa/s		
Minimum Rise/Fall Time	Typical (1Vpp) <5ns		
Jitter (rms)	Typical (1Vpp) ≤5MHz 2ppm+500ps > 5MHz 500ps		
Interpolation Method	Off, Linear		
Edit Method	Edit Points, Edit Block		
<b>Harmonic</b>			
Harmonic Order	≤16		
Harmonic Type	Even, Odd, All, User		
Harmonic Amplitude	can be set for all harmonics		
Harmonic Phase	can be set for all harmonics		
<b>Output Characteristics</b>			
<b>Amplitude (into 50 Ω)</b>			
Range	≤20MHz: 1mVpp to 10Vpp ≤70MHz: 1mVpp to 5Vpp ≤120MHz: 1mVpp to 2.5Vpp ≤160MHz: 1mVpp to 1Vpp	≤20MHz: 1mVpp to 10Vpp ≤70MHz: 1mVpp to 5Vpp ≤100MHz: 1mVpp to 2.5Vpp	≤20MHz: 1mVpp to 10Vpp ≤60MHz: 1mVpp to 5Vpp
Accuracy	Typical (1kHz Sine, 0V Offset, >10mVpp, Auto) ± 1% of setting ± 2mV		
Flatness (relative to 1kHz Sine wave, 500 mVpp, 50Ω)	Typical ≤10MHz: ±0.1dB ≤60MHz: ±0.2dB ≤100MHz: ±0.4dB ≤160MHz: ±0.8dB	Typical ≤10MHz: ±0.1dB ≤60MHz: ±0.2dB ≤100MHz: ±0.4dB	Typical ≤10MHz: ±0.1dB ≤60MHz: ±0.2dB
Units	Vpp, Vrms, dBm		
Resolution	1mV or 3bits		

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<b>Offset (into 50 <math>\Omega</math>)</b>	
Range	$\pm 5V_{pk}$ ac + dc
Accuracy	1% of setting + 5mV + 0.5% of amplitude
<b>Waveform Output</b>	
Impedance	50 $\Omega$ (Typical)
Protection	Short-circuit protection, automatically disable waveform output when overload occurs
<b>Modulation Characteristics</b>	
Modulation Type	AM, FM, PM, ASK, FSK, PSK, BPSK, QPSK, 3FSK, 4FSK, OSK, PWM
<b>AM</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal/External
Modulating Waveform	Sine, Square, Ramp, Noise, Arb
Depth	0% to 120%
Modulating Frequency	2mHz to 50KHz
<b>FM</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal/External
Modulating Waveform	Sine, Square, Ramp, Noise, Arb
Modulating Frequency	2mHz to 50KHz
<b>PM</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal/External
Modulating Waveform	Sine, Square, Ramp, Noise, Arb
Phase Deviation	0° to 360°
Modulating Frequency	2mHz to 50KHz
<b>ASK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal/External
Modulating Waveform	Square with 50% duty cycle
Key Frequency	2mHz to 1MHz
<b>FSK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal/External
Modulating Waveform	Square with 50% duty cycle

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Key Frequency	2mHz to 1MHz
<b>3FSK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal
Modulating Waveform	Square with 50% duty cycle
Key Frequency	2mHz to 1MHz
<b>4FSK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal
Modulating Waveform	Square with 50% duty cycle
Key Frequency	2mHz to 1MHz
<b>PSK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal/External
Modulating Waveform	Square with 50% duty cycle
Key Frequency	2mHz to 1MHz
<b>BPSK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal
Modulating Waveform	Sine, Square, Ramp, Noise, Arb (2mHz to 50kHz)
Key Frequency	2mHz to 1MHz
<b>QPSK</b>	
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)
Source	Internal
Modulating Waveform	Sine, Square, Ramp, Noise, Arb (2mHz to 50kHz)
Key Frequency	2mHz to 1MHz
<b>OSK</b>	
Carrier Waveform	Sine
Source	Internal/External
Oscillation Time	8ns to 200s
Key Frequency	2mHz to 1MHz
<b>PWM</b>	
Carrier Waveform	Pulse
Source	Internal/External
Modulating Waveforms	Sine, Square, Ramp, Noise, Arb
Width Deviation	0% to 100% of Pulse Width

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Modulating Frequency	2mHz to 50KHz		
<b>[Mod/FSK/Trig] Input</b>			
Input Range	75mVRMS to $\pm 2.5V_{ac}+dc$		
Input Bandwidth	5MHz		
Input Impedance	100 $\Omega$		
<b>Burst Characteristics</b>			
Carrier Waveform	Sine, Square, Ramp, Pulse, Noise, Arb (except DC)		
Carrier Frequency	2mHz to 100MHz	2mHz to 100MHz	2mHz to 60MHz
Burst Count	1 to 1 000 000 or Infinite		
Start/Stop Phase	0° to 360°		
Internal Period	2 $\mu$ s to 500s		
Gated Source	External Trigger		
Trigger Source	Internal, External or Manual		
Trigger Delay	0ns to 85s		
<b>Sweep Characteristics</b>			
Carrier Waveform	Sine, Square, Ramp, Arb (except DC)		
Type	Linear, Log or Step		
Direction	Up or Down		
Start/Stop Frequency	1 $\mu$ Hz to 160MHz	1 $\mu$ Hz to 100MHz	1 $\mu$ Hz to 60MHz
Sweep Time	1ms to 300s		
Hold/Return Time	0ms to 300s		
Trigger Source	Internal, External or Manual		
Mark	Falling edge of Sync signal (programmable)		
<b>Counter Specifications</b>			
Function	Frequency, Period, Positive/Negative Pulse Width, Duty Cycle		
Frequency Resolution	6 digits/second (Gate Time = 1s)		
Frequency Range	1 $\mu$ Hz to 200MHz		
Period Measurement	Measurement Range	5ns to 16 days	
Voltage Range and Sensitivity (Not modulation signal)			
DC Coupling	DC Offset Range	$\pm 1.5V_{DC}$	Input Attenuation: "closed"
	1 $\mu$ Hz to 100MHz	50mVRMS to $\pm 2.5V_{ac}+dc$	

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	100MHz to 200MHz	100mVRMS to $\pm 2.5V_{ac+dc}$	
AC Coupling	1 $\mu$ Hz to 100MHz	50mVRMS to $\pm 2.5V_{pp}$	
	100MHz to 200MHz	100mVRMS to $\pm 2.5V_{pp}$	
Pulse Width and Duty Cycle Measurement			
Frequency/Amplitude Range	1 $\mu$ Hz to 25MHz	50mVRMS to $\pm 2.5V_{ac+dc}$	DC Coupling Input Attenuation: "closed"
Pulse Width	Minimum	$\geq 20ns$	
	Resolution	2ns	
Duty Cycle	Range (Display)	0% to 100%	
Input Characteristics			
Input Range	Breakdown Voltage	$\pm 7V_{ac+dc}$ (Attenuation: closed)	Impedance=1M $\Omega$
		$\pm 70V_{ac+dc}$ (Attenuation: open)	
		5Vrms	Impedance=50 $\Omega$
Input Adjustment	Attenuation	Open: "x10"; Closed: "x1"	
	Impedance	50 $\Omega$	1M $\Omega$
	Coupling	AC	DC
	HF Reject	ON: input bandwidth=250KHz; OFF: input bandwidth=225MHz	
Input Trigger	Trigger Level Range	-2.5V to +2.5V	
	Trigger Sensitivity Range	0% (140mV hysteresis voltage) to 100% (2mV hysteresis voltage)	
Gate Time	GateTime1	1.310ms	
	GateTime2	10.48ms	
	GateTime3	166.7ms	
	GateTime4	1.342s	
	GateTime5	10.73s	
	GateTime6	>10s	

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<b>Trigger Characteristics</b>	
<b>Trigger Input</b>	
Level	TTL-compatible
Slope	Rising or falling (selectable)
Pulse Width	> 50ns
Latency	Sweep: <100ns (typical) Burst: <300ns (typical)
<b>Trigger Output</b>	
Level	TTL-compatible
Pulse Width	> 60ns (typical)
Maximum Rate	1MHz
<b>Clock Reference</b>	
<b>Phase Offset</b>	
Range	0° to 360°
Resolution	0.03°
<b>External Reference Input</b>	
Lock Range	10MHz ± 50Hz
Level	250mVpp to 5Vpp
Lock Time	< 2s
Impedance (Typical)	1kΩ, AC coupling
<b>Internal Reference Output</b>	
Frequency	10MHz ± 50Hz
Level	3.3Vpp
Impedance (Typical)	50Ω, AC coupling
<b>Sync Output</b>	
Level	TTL-compatible
Impedance	50 Ω, nominal value
<b>General Specifications</b>	
<b>Power</b>	
Power Voltage	100V to 240V (45Hz to 440Hz)
Power Consumption	Less than 50W
Fuse	250V, T2A
<b>Display</b>	

## Rigol DG4000 Series Waveform Generator Specifications

Type	7-inch TFT LCD
Resolution	800 Horizontal × RGB × 480 Vertical Resolution
Color	16M color
<b>Environment</b>	
Temperature Range	Operating: 10°C to 40°C Non-Operating: -20°C to 60°C
Cooling Method	Cooling by fans compulsively
Humidity Range	Less than 35°C: ≤90% Relative Humidity (RH) 35°C to 40°C: ≤60% Relative Humidity (RH)
Altitude	Operating: Less than 3000 meters Non-Operating: Less than 15000 meters
<b>Mechanical</b>	
Dimensions (W×H×D)	313 mm ×160.7 mm×116.7mm
Weight	without package: 3.2 kg with package: 4.5 kg
<b>Interfaces</b>	
USB Host, USB Device, LAN	
<b>IP Protection</b>	
IP2X	
<b>Calibration Interval</b>	
Recommend 1 year for standard interval	