Chapter 18 Specifications

All the specifications are guaranteed except the parameters marked with "Typical" and the oscilloscope needs to operate for more than 30 minutes under the specified operation temperature.

Sample

<u>oumpro</u>			
Sample Mode	Real-time Sample		
Real Time	Analog channe: 2 GSa/s (single-channel), 1 Gsa/s		
	(dual-channel)		
Sample Rate	Digital channel: 1 GSa/s (max)		
Peak Detect	Analog channe: 500 ps (single-channel), 1 ns (dual-channel)		
	Digital channel: 2 ns		
	After both the channels finish N samples at the same time, N		
Averaging	can be 2, 4, 8, 16, 32, 64, 128, 256, 512, 1024, 2048, 4096 or		
	8192.		
Libria Danah dian	12 bits of resolution when ≥5 µs/div @ 1 GSa/s (or ≥10 µs/div		
High Resolution	@ 500 MSa/s).		
Min Detect Pulse	Digital channel: 5 ns		
Width			
	Analog channel:		
Memory Depth	single-channel: Auto, 14k pts, 140k pts, 1.4M pts, 14M pts and		
	56M pts (option) are available		
	dual-channel: Auto, 7k pts, 70k pts, 700k pts, 7M pts and 28M		
	pts (option) are available		
	Digital channel: 14M pts maximum		

Input

Impat		
Number of	MSO2XX2A/2XX2A-S: 2 analog channels+16 digital channels	
Channels	DS2XX2A/2XX2A-S: 2 analog channels	
Input Coupling	DC, AC or GND	
Input Impedance	Analog channe: (1 M Ω ±1%) (16 pF±3 pF) or 50 Ω ±1.5%	
	Digital channel: (101 k Ω ±1%) (9 pF±1 pF)	
Probe	Analog channe: 0.01X to 1000X, in 1-2-5 step	
Attenuation		
Coefficient		
Maximum Input	Maximum Input Voltage of the Analog Channel	
Voltage (1 MΩ)	CAT I 300 Vrms, CAT II 100 Vrms,	
	Transient Overvoltage 1000 Vpk	
	Digital channel: CAT I 40Vrms, Transient Overvoltage 800 Vpk	

Horizontal

Time Base Scale	MSO/DS2302A/2302A-S: 1.000 ns/div to 1.000 ks/div	
	MSO/DS2202A/2202A-S: 2.000 ns/div to 1.000 ks/div	
	MSO/DS2102A/2102A-S/2072A/2072A-S:	
	5.000 ns/div to 1.000 ks/div	
Deviation	1 ns (typical), 2 ns (max)	
between		
Channels		
Max Record	14 Mpts	
Length		
Time Base	≤ ± 25 ppm	
Accuracy ^[1]		
Clock Drift	≤ ± 5 ppm/year	
Max Delay Range	Negative delay: ≥1 screen width	
	Positive delay: 1 s to 100 ks	
Time Base Mode	Y-T, X-Y, Roll	
Number of XYs	1	
Waveform	50,000 wfms/s (dots display)	
Capture Rate ^[2]		
Length Time Base Accuracy ^[1] Clock Drift Max Delay Range Time Base Mode Number of XYs Waveform	≤ ± 25 ppm ≤ ± 5 ppm/year Negative delay: ≥1 screen width Positive delay: 1 s to 100 ks Y-T, X-Y, Roll 1	

Vertical

verticai			
Bandwidth	MSO/DS2302A/2302A-S: DC to 300 MHz		
(-3 dB) (50 Ω)	MSO/DS2202A/2202A-S: DC to 200 MHz		
	MSO/DS2102A/2102A-S: DC to 100 MHz		
	MSO/DS2072A/2072A-S: DC to 70 MHz		
Single-shot	MSO/DS2302A/2302A-S: DC to 300 MHz		
Bandwidth (50 Ω)	MSO/DS2202A/2202A-S: DC to 200 MHz		
	MSO/DS2102A/2102A-S: DC to 100 MHz		
	MSO/DS2072A/2072A-S: DC to 70 MHz		
Vertical	Analog channe: 8 bit		
Resolution	Digital channel: 1 bit		
Vertical Scale	When the input impedance is 50Ω: 500 μV/div to 1 V/div		
	When the input impedance is 1MΩ: 500 μV/div to 10 V/div		
Offset Range	When the input impedance is 50Ω :		
	500 μV /div to 50 mV/div: ± 2 V		
	51 mV/div to 200 mV/div: ± 10 V		
	205 mV/div to 1 V/div: ± 12 V		
	When the input impedance is $1M\Omega$:		
	500 μV /div to 50 mV/div: ± 2 V		
	51 mV/div to 200 mV/div: ± 10 V		
	205 mV/div to 2 V/div: ± 50 V		
	2.05 V/div to 10 V/div: ± 100 V		
Bandwidth	MSO/DS2302A/2302A-S/2202A/2202A-S: 20 MHz/100 MHz		
Limit ^[1]	MSO/DS2102A/2102A-S/2072A/2072A-S: 20 MHz		
Low Frequency			
Response	≤5 Hz (on BNC)		
(AC Coupling,			
-3 dB)			
Calculated Rise	MSO/DS2302A/2302A-S: 1.2ns		
Time ^[1]	MSO/DS2202A/2202A-S: 1.8 ns		
	MSO/DS2102A/2102A-S: 3.5 ns		
	MSO/DS2072A/2072A-S: 5 ns		
DC Gain Accuracy	±2% full scale		
DC Offset	$\pm 0.1 \text{ div } \pm 2 \text{ mV } \pm 1\% \text{ offset value}$		
Accuracy			
Channel to	DC to maximum bandwidth: >40 dB		
Channel Isolation			

Vertical (Digital Channel) (MSO2000A and MSO2000A-S)

vertical (Digital Charmer) (W302000A and W302000A-3)		
Threshold	Adjustable threshold of 8 channels per group	
Threshold	TTL (1.4 V)	
Selection	5.0 V CMOS (+2.5 V)	
	3.3 V CMOS (+1.65 V)	
	2.5 V CMOS (+1.25 V)	
	1.8 V CMOS (+0.9 V)	
	ECL (-1.3 V)	
	PECL (+3.7 V)	
	LVDS (+1.2 V)	
	0 V	
	User Def	
Threshold Range	±20.0 V, 10 mV step	
Threshold	±(100 mV+3% threshold setting)	
Accuracy		
Dynamic Range	±10 V + Threshold	
Minimum Voltage	500 mVpp	
Swing		
Input Resistance	//101 ΚΩ	
Probe Load	≈8 pF	
Vertical	1 bit	
Resolution		

Trigger

Trigger			
Trigger Level	Internal	± 5 div from center of the screen	
Range	EXT	± 4 V	
Trigger Mode	Auto, Normal, Single		
Holdoff Range	100 ns to 10 s		
High Frequency	75 kHz		
Rejection ^[1]			
Low Frequency	75 kHz		
Rejection ^[1]			
Trigger	· ·	10 mV or noise rejection is enabled)	
Sensitivity	0.3 div (abov	ve 10 mV and noise rejection is disabled)	
Edge Trigger	T		
Edge Type	Rising, Falling	g, Rising/Falling	
Pulse Trigger			
Pulse Condition	Positive Pulse	e Width (greater than, lower than, within specific	
	interval)		
	_	se Width (greater than, lower than, within specific	
	interval)		
Pulse Width	2 ns to 4 s		
Range			
Runt Trigger	T		
Pulse Width	None, >, <, <>		
Condition			
Pulse Polarity	Positive, Negative		
Pulse Width	2 ns to 4 s		
Range			
Windows Trigger (Option)			
Windows Type	Rising, Falling, Rising/Falling		
Trigger Position	Enter, Exit, Time		
Windows Time	16 ns to 4 s		
Nth Edge Trigger (Option)			
Edge Type	Rising, Falling		
Idle Time	16 ns to 4 s		
Edge Number	1 to 65535		
Slope Trigger	T		
Slope Condition	Positive Slope (greater than, lower than, within specific		

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	interval)		
	Negative Slope (greater than, lower than, within specific		
	interval)		
Time Setting	10 ns to 1 s		
Video Trigger (H	DTV Option)		
Signal Standard	NTSC, PAL/SECAM, 480P, 576P (Standard)		
	720P, 1080P and 1080I (Option)		
Pattern Trigger			
Pattern Setting	H, L, X, Rising, Falling		
Delay Trigger (O	ption)		
Edge Type	Rising, Falling		
Delay Type	>, <, <>, ><		
Delay Time	2 ns to 4 s		
TimeOut Trigger	(Option)		
Edge Type	Rising, Falling, Rising/Falling		
Timeout time	16 ns to 4 s		
Duration Trigger (Option)			
Pattern	H, L, X		
Trigger Condition	>, <, <>		
Duration Time	2 ns to 4 s		
Setup/Hold Trig	ger		
Edge Type	Rising, Falling		
Data Type	H, L		
Setup Time	2 ns to 1 s		
Hold Time	2 ns to 1 s		
RS232/UART Tri	gger		
Polarity	Normal, Invert		
Trigger Condition	Start, Error, Check Error, Data		
Baud Rate	2400 bps, 4800 bps, 9600 bps, 19200 bps, 38400 bps, 57600		
	bps, 115200 bps, 230400 bps, 460800 bps, 921600 bps,		
	1Mbps, User		
Data Bits	5 bit, 6 bit, 7 bit, 8 bit		
I2C Trigger			
Trigger Condition	Start, Restart, Stop, Missing ACK, Address, Data, A&D		
Address Bits	7 bit, 8 bit, 10 bit		
Address Range	0 to 127, 0 to 255, 0 to 1023		

Byte Length	1 to 5	
SPI Trigger		
Trigger Condition	Timeout	
Timeout Value	100 ns to 1 s	
Data Bits	4 bit to 32 bit	
Data Line Setting	H, L, X	
CAN Trigger (Option)		
Signal Type	Rx, Tx, CAN_H, CAN_L, Differential	
Trigger Condition	SOF, EOF, FrameType, FrameError	
Signal Rate	10 kbps, 20 kbps, 33.3 kbps, 50 kbps, 62.5 kbps, 83.3 kbps,	
	100 kbps, 125 kbps, 250 kbps, 500 kbps, 800 kbps, 1 Mbps,	
	User	
Sample Points	5% to 95%	
Frame Type	Data, Remote, Error, OverLoad	
Error Type	Bit Fill, AnswerError, CheckError, FormatError, RandomError	
USB Trigger (Option)		
Signal Speed	Low Speed, Full Speed	
Trigger condition	SOP, EOP, RC, Suspend, Exit Suspend	

Measure

ivicasai c		T		
Cursor		Voltage Deviation between Cursors (△V)		
	Manual Mode	Time Deviation between Cursors (ΔT)		
		Reciprocal of ΔT (Hz) (1/ΔT)		
	Track Mode	Voltage and Time Values of the Waveform		
		Point		
	Auto Mode	Allow to display cursors during auto		
		measurement		
Auto	Analog channel	Analog channel:		
Measurement	Maximum, Minii	mum, Peak-Peak Value, Top Value, Bottom		
	Value, Amplitud	e, Average, Vrms-N, Vrms-1, Overshoot,		
	Pre-shoot, Area	Pre-shoot, Area, Period Area, Period, Frequency, Rise Time,		
	Fall Time, Positi	ve Pulse Width, Negative Pulse Width, Positive		
	Duty Cycle, Negative Duty Cycle, Delay Af→Bf, Delay			
	At→Bt, Delay	Af→Bt, Delay At→Bf, Phase Af→Bf,		
	Phase At→Bt, Phase Af→Bt, Phase At→Bf Digital channel:			
	Frequency, Period, Positive Pulse Width, Negative Pulse Width,			
	Positive Duty Cycle, Negative Duty Cycle, Delay Af→Bf,			
	Delay At→Bt,	Delay Af→Bt, Delay At→Bf, Phase		
	$Af \rightarrow Bf$, Phase $At \rightarrow Bt$, Phase $Af \rightarrow Bt$, Phase $At \rightarrow Bf$			
Number of	Display 5 measi	Display 5 measurements at the same time.		
Measurements	Bisplay o meast	Display 3 measurements at the same time.		
Measurement	Screen or curso	Screen or cursor		
Range	Screen or carso			
Measurement	Current Value, A	Current Value, Average, Max, Min, Standard Deviation,		
Statistic	Number of Mea	Number of Measurements		
Frequency	Hardware 6 bits	Hardware 6 bits frequency counter		
Counter	(channels are selectable)			

Math Operation

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Waveform	A+B, A-B, A×B, A÷B, FFT, Digital Filter, Editable Advanced	
Operation	Operation, Logic Operation	
FFT Window	Rectangle, Hanning, Blackman, Hamming	
Function		
FFT Display	Split, Full Screen	
FFT Vertical Scale	Vrms, dB	
Logic Operation	AND, OR, NOT, XOR	
Math Function	Intg, Diff, Log, Exp, Sqrt, Sine, Cosine, Tangent	
Number of Buses	2	
for Decoding		
Decoding Type	Parallel (standard), RS232/UART (option), I2C (option), SPI	
	(option), CAN (option)	

Display

Screen Type	8.0 inches (203 mm) TFT LCD display	
Display	800 Horizontal ×RGB×480 Vertical Pixel	
Resolution		
Display Color	160,000 Color (TFT)	
Persistence Time	Min, 50ms, 100ms, 200ms, 500ms, 1 s, 2 s, 5 s, 10 s, 20 s,	
	Infinite	
Display Type	Dots, Vectors	
Real-time Clock	Time and Date (user adjustable)	

Signal Source (MSO2000A-S/DS2000A-S)

Signal Source	(MSO2000A-S/	DS2000A-S)
Channels	2	
Sample Rate	200 MSa/s	
Vertical	14 bits	
Resolution		
Max. Frequency	25 MHz	
Standard	Sine, Square, Pu	lse, Ramp, Noise, DC
Waveform		
Built-in	Sinc, Exponentia	l Rise, Exponential Fall, ECG, Gauss, Lorentz,
Waveform	Haversine	
Sine	Frequency	100 mHz to 25 MHz
	Range	
	Flatness	±0.5 dB (relative to 1 kHz)
	Harmonic	-40 dBc
	Distortion	
	Stray	-40 dBc
	(Non-harmonic)	
	Total Harmonic	1%
	Distortion	
	S/N Ratio	40 dB
Square/Pulse	Frequency	Square: 100 mHz to 15 MHz
	Range	Pulse: 100 mHz to 1 MHz
	Rise/Fall Time	<15 ns
	Overshoot	<5%
	Duty Cycle	Square: 50%
		Pulse: 10% to 90% (user adjustable)
	Duty Cycle	1% or 10 ns (the larger of the two)
	Resolution	
	Min. Pulse	20ns
	Width	
	Pulse Width	10 ns or 5 bits (the larger of the two)
	Resolution	
	Jitter	500 ps
Ramp	Frequency	100 mHz to 100 kHz
	Range	
	Linearity	1%

	1	T
	Symmetry	0 to 100%
Noise	Bandwidth	25 MHz (typical)
Built-in	Frequency	100 mHz to 1 MHz
Waveform	Range	
Arbitrary	Frequency	100 mHz to 10 MHz
Waveform	Range	
	Waveform	1 to 16 k points
	Length	
	Internal	10
	Storage	
	Location	
Frequency	Accuracy	100 ppm (lower than 10 kHz)
		50 ppm (higher than 10 kHz)
	Resolution	100 mHz or 4 bits, the larger of the two
Amplitude	Output Range	20 mVpp to 5 Vpp, HighZ
		10 mVpp to 2.5 Vpp, 50 Ω
	Resolution	100 µV or 3 bits, the larger of the two
	Accuracy	2% (1 kHz)
DC Offset	Range	±2.5 V, HighZ
		±1.25 V, 50 Ω
	Resolution	100 µV or 3 bits, the larger of the two
	Accuracy	Offset setting Value±2%

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Standard Ports	USB HOST (support USB-GPIB), USB DEVICE, LAN, Aux		
	Output (TrigOut/PassFail)		
Printer	PictBridge		
Compatibility			

General Specifications

General Specifications					
Probe Compensation	n Output				
Output Voltage ^[1]	About 3 V, peak-peak				
Frequency ^[1]	1 kHz				
Power					
Power Voltage	100 V to 240 V, 45 Hz to 440 Hz				
Power	Maximum 50 W				
Fuse	2 A, T Degree, 250 V				
Environment					
Temperature Range	Operating: 0°C to +50°C				
	Non-operating: -40°C	to +70℃			
Cooling Method	Fan cooling				
Humidity Range	0°C to +30°C: ≤95% Relative Humidity				
	+30°C to +40°C: ≤75% Relative Humidity				
	+40°C to +50°C: ≤45	5% Relative Humidity			
Altitude	Operating: under 3,000 meters				
	Non-operating: under 15,000 meters				
Physical Characteris	stics				
Size ^[3]	Width \times Height \times Depth = 361.6 mm \times 179.6 mm \times				
	130.8 mm				
Weight ^[4]	Package Excluded	$3.9 \text{ kg} \pm 0.5 \text{ kg}$			
	Package Included	$4.5 \text{ kg} \pm 0.5 \text{ kg}$			
Calibration Interval					
The recommended cal	ibration interval is one y	ear.			
Regulatory Informa	tion				
Electromagnetic	2004/108/EC				
Compatibility	Execution standard EN 61326-1:2006 EN 61326-2-1:2006				
Safety	N/CSA-C22.2 NO. 61010-1-2004;				
	EN 61010-1:2001; IEC 61010-1:2001				

Note^[1]: Typical.

Note^[2]: Maximum value. 20ns, single-channel mode, dots display, auto memory depth.

Note^[3]: Supporting legs and handle folded, knob height included.

Note^[4]: Standard configuration.