

# Serial Bus Applications for InfiniiVision 3000 X-Series Oscilloscopes

Data Sheet



### **Supported protocols and features**

- I<sup>2</sup>C
- SPI
- RS232/UART
- I<sup>2</sup>S
- CAN
- LIN
- FlexRay
- MIL-STD 1553
- ARINC 429
- · Hardware-based decoding
- Multi-bus analysis
- Automatic search and navigation
- · Compatibility with segmented memory acquisition
- Eye-diagram mask files available for CAN, FlexRay, MIL-STD 1553, and ARINC 429
  - (requires DSOX3MASK mask test option)

## Introduction

Serial buses are pervasive in today's digital designs and are used for a variety of purposes including on-board chipto-chip communication, CPU to peripheral control, as well as for remote sensor data transfer and control. Without intelligent oscilloscope serial bus triggering and protocol decode, it can be difficult to debug these buses and correlate data transfers with other mixed signal interactions in your system. Agilent's InfiniiVision 3000 X-Series oscilloscopes (DSOs) and mixed signal oscilloscopes (MSOs) offer optional integrated serial bus triggering and hardware-based protocol decoding solutions that give you the tools you need to accelerate debug of your designs that include serial bus communication.





Agilent Technologies

#### Hardware-Based Decoding

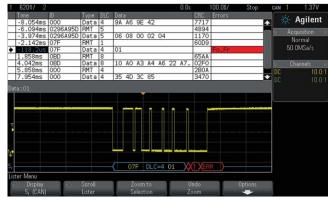
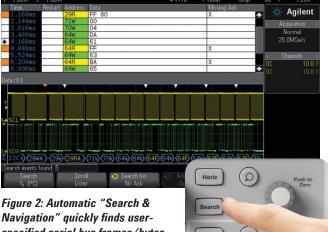


Figure 1: Hardware-based decoding quickly reveals serial communication errors.

Agilent's InfiniiVision Series oscilloscopes are the industry's only scopes to use hardware-based decoding. Other vendor's scopes with serial bus triggering and protocol decode, use software post-processing techniques to decode serial packets/frames. With these software techniques, waveformand decode-update rates tend to be slow (sometimes seconds per update.) That's especially true when using deep memory, which is often required to capture multiple packetized serial bus signals. And when analyzing multiple serial buses simultaneously, software techniques can make decode update rates even slower.

Faster decoding with hardware-based technology enhances scope usability, and more importantly, the probability of capturing infrequent serial communication errors. Figure 1 shows an example of an Agilent 3000 X-Series scope capturing a random and infrequent CAN error frame. The upper half of the scope's display shows the decoded data in a "Lister" format, along with a time-correlated decode trace shown below the waveform.

#### **Automatic Search and Navigation**



Navigation" quickly finds userspecified serial bus frames/bytes of interest.

After capturing a long record of serial bus communication using the InfiniiVision scope's *MegaZoom* deep memory. you can easily perform a search operation based on specific criteria that you enter. Then, you can quickly navigate to bytes/frames of serial data that satisfy the entered search criteria. Figure 2 shows an example of searching on captured I<sup>2</sup>C data to find all occurrences of Read or Write operations with "No Ack." In this case, the scope found five occurrences of data transfers with "No Ack," and marked each occurrence with a white triangle to show where in time they happened relative to the captured waveform. Navigating and zooming-in on each marked byte/frame is guick and easy using the scope's front panel navigation keys.

#### **Multi-bus Analysis**

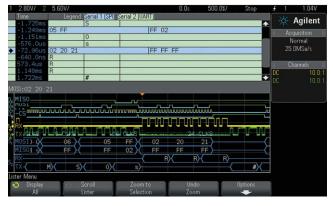


Figure 3: An interleaved "Lister" makes it easier to time-correlate activity between two decoded serial buses.

Many of today's designs include multiple serial buses. Sometimes it may be necessary to correlate data from one serial bus to another. Agilent's InfiniiVision 3000 X-Series oscilloscope can decode two serial buses simultaneously using hardware-based decoding. Plus it is the only scope on the market that can also display the captured data in a time-interleaved "Lister" display, as shown in Figure 3. In this particular example, the scope has decoded a 4-wire SPI bus in a HEX format, along with transmit and receive RS232/ UART signals in an ASCII-decoded format.

#### Using Segmented Memory to Capture Multiple Serial Bus Packets

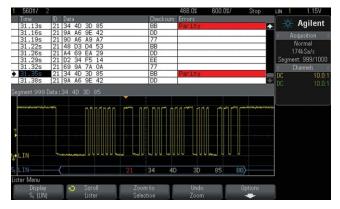


Figure 4: Segmented memory acquisition selectively captures more packets/bytes of serial bus activity.

The segmented memory option for Agilent's InfiniiVision 3000 X-Series oscilloscope can optimize your scope's memory, letting you capture more packets/frames of serial bus activity. Segmented memory acquisition optimizes the number of packetized serial communication frames that can be captured consecutively. Segmented memory does this by capturing just the selective frames/bytes of interest while ignoring (not digitizing) idle time and other unimportant frames/bytes. Figure 4 shows an example of the 3000 X-Series oscilloscope capturing 1,000 consecutive LIN serial bus frames qualified with a 21<sub>HEX</sub> frame ID for a total acquisition time of over 30 seconds. Capturing this much data using conventional oscilloscope acquisition memory would be impossible.

Agilent's InfiniiVision Series oscilloscopes are the only scopes on the market today that can acquire segments on up to four analog channels of acquisition, and time-correlated segments on digital channels (using an MSO model), along with automatic hardware-based serial bus decoding for each segment. In addition, you can use the scope's Search & Navigation capability after a segmented memory acquisition has been performed.

#### Serial Bus Eye-diagram and Pulse Mask Testing

With the addition of the DSOX3MASK mask test option, which can perform over 200,000 pass/fail tests per second, you can perform eye-diagram and pulse mask testing on CAN, FlexRay, MIL-STD 1553, and ARINC 429 signals. Eye-diagram measurements provide a comprehensive signal quality test of the integrity of your transmitted and received signals. Agilent provides various mask files that you can download at no charge. The mask files are based on published industry mask standards and/or derived from physical layer/electrical specifications.

#### The following CAN mask files are available:

- 125 kbps 400 meters
- 250 kbps 200 meters
- 500 kbps 10 meters
- 500 kbps 80 meters
- 800 kbps 40 meters
- 1000 kbps 25 meters

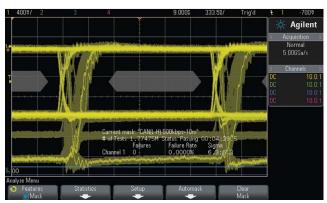


Figure 5: CAN 500 kbps mask test on 10 meter system

#### The following FlexRay mask test files are available:

- TP1 standard voltage (10 Mbps only)
- TP1 increased voltage (10 Mbps only)
- TP11 standard voltage (10 Mbps only)
- TP11 increased voltage (10 Mbps only)
- TP4 10 Mbps
- TP4 5 Mbps
- TP4 2.5 Mbps

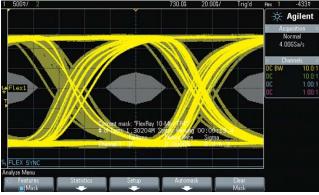


Figure 6: FlexRay TP4 eye-diagram mask test.

#### The following MIL-STD 1553 mask test files are available:

- System xfmr-coupled Input
- · System direct-coupled Input
- BC xfmr-coupled Input
- BC direct-coupled Input
- RT xfmr-coupled Input
- · RT direct-coupled Input

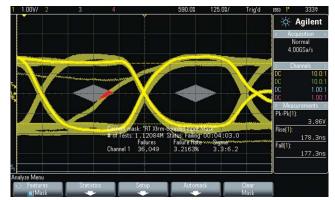


Figure 7: MIL-STD 1553 BC to RT xfrm-coupled input mask test reveals a shifted bit that violates the pass/fail mask.

# The following ARINC 429 mask/pulse test files are available:

- · 100 kbps Eye Test
- 100 kbps 1's Pulse Test
- 100 kbps 0's Pulse Test
- · 100 kbps Null Level Test
- · 12.5 kbps Eye Test
- 12.5 kbps 1's Pulse Test
- 12.5 kbps 0's Pulse Test
- · 12.5 kbps Null Level Test

For additional information about eye-diagram mask testing on CAN, FlexRay, MIL-STD 1553, and ARINC 429 signals, refer to the application notes listed at the end of this document.

# 1 4.00 2 3 4 2.500 1.000 7 Trig'd 43 1\* 3.00 2 Active to the second se

Figure 8: ARINC 429 100 kbps eye-diagram mask test.

#### **Probing Differential Serial Buses**

Some of today's serial buses are based on differential signaling, such as CAN, FlexRay, MIL-STD 1553, and ARINC 429. Probing differential serial buses such as these requires that you use a differential active probe. Agilent offers a range of differential active probes compatible with the InfiniiVision 3000 X-Series oscilloscopes for various bandwidth and dynamic range applications.

For CAN, MIL-STD 1553, and ARINC 429 differential bus applications, Agilent recommends the 25-MHz bandwidth N2791A differential active probe shown in Figure 9.

For both CAN and FlexRay applications, Agilent recommends the 200-MHz bandwidth N2792A differential active probe shown in Figure 10. Also available for FlexRay applications is the 800-MHz bandwidth N2793A differential active probe.

If you need to connect to DB9-SubD connectors on your differential CAN and/or FlexRay bus, Agilent also offers the CAN/FlexRay DB9 probe head (Part number 0960-2926). This differential probe head, which is shown in the insert of Figure 9, is compatible with both the N2791A and N2792A differential active probes and allows you to connect easily to your CAN and/or FlexRay differential bus.

If you already own a significant installed-base of Tektronix active probes, Agilent offers a Tek-to-Agilent probe adapter (N2744A). Agilent also offers a variety of higher bandwidth differential and single-ended active probes not shown here.

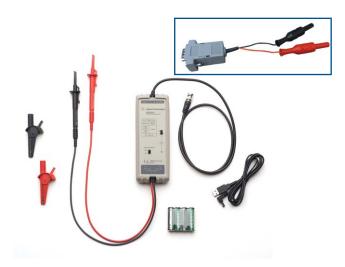
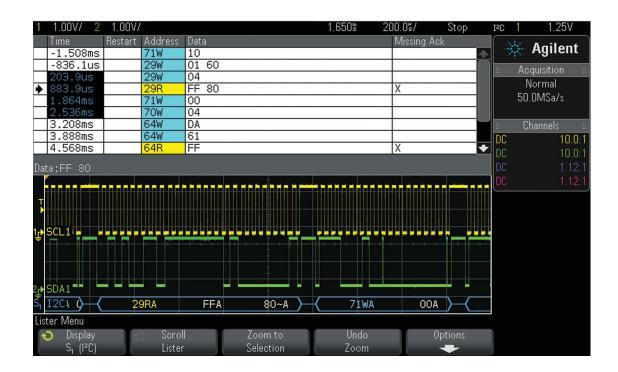


Figure 9: Agilent N2791A 25-MHz differential active probe.



Figure 10: Agilent N2792A 200-MHz differential active probe.

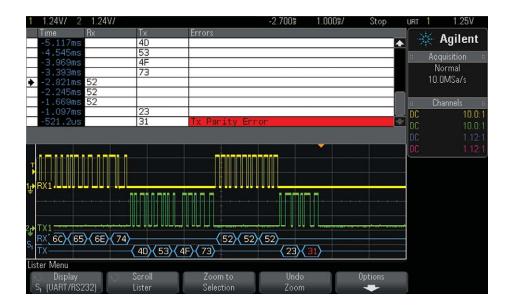
Clock and data input source	Appleg shappels 1, 2, 2, or 4
Clock and data input source	Analog channels 1, 2, 3, or 4
	Digital channels D0 to D15
Max clock/data rate	Up to 3.4 Mbps
Triggering	Start condition
	Stop condition
	Missing acknowledge
	Address with no acknowledge
	Restart
	EEPROM data read
	Frame (Start:Addr7:Read:Ack:Data)
	Frame (Start:Addr7:Write:Ack:Data)
	Frame (Start:Addr7:Read:Ack:Data:Ack:Data2)
	Frame (Start:Addr7:Write:Ack:Data:Ack:Data2)
	10-bit write
Hardware-based decode	Data (HEX digits in white)
	Address decode size: 7 bits (excludes R/W bit) or 8 bits (includes R/W bit)
	Read address (HEX digits followed by "R" in yellow)
	Write address (HEX digits followed by "W" in light-blue)
	Restart addresses ("S" in green, followed by HEX digits, followed by "R" or "W")
	Acknowledges (suffixes "A" or "~A" in the same color as the data or address preceding it)
	ldle bus (mid-level bus trace in dark blue)
	Active bus (bi-level bus trace in dark blue)
	Unknown/error bus (bi-level bus trace in red)
Multi-bus analysis	l <sup>2</sup> C plus one other serial bus (including another l <sup>2</sup> C bus)
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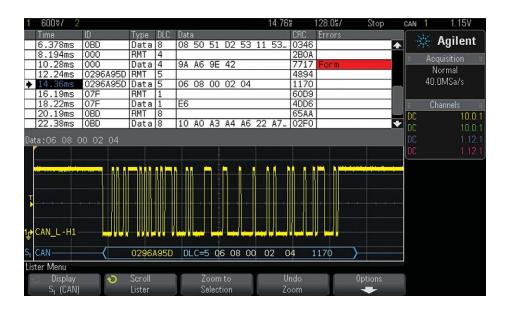
SPI specifications/characteristics (DS0X3EMBD)		
MOSI, MISO, Clock, and CS input source	Analog channels 1, 2, 3, or 4 Digital channels D0 to D15	
Max clock/data rate	Up to 25 Mb/s	
Triggering	4- to 64-bit data pattern during a user-specified framing period Framing period can be a positive or negative chip select (CS or ~CS) or clock idle time (timeout)	
Hardware-based decode	Number of decode traces: 2 independent traces (MISO and MOSI) Data (hex digits in white) Unknown/error bus (bi-level bus trace in red) Number of clocks/packet ("XX CLKS" in light-blue above data packet) Idle bus (mid-level bus trace in dark blue) Active bus (bi-level bus trace in dark blue)	
Multi-bus analysis	SPI plus one other serial bus (excluding another SPI bus)	

1 2 6.980° 500.0°/ Stop	spi D <sub>e</sub>	TTL
Time MOSI	340	Agilant
-8.628ms 02 08 49 4C 45 4E 54 FF FF FF FF FF FF FF FF FF	N 100 K	Agilent
-3.924ms 03 06 00 00 00 00 00 00 00 FF FF 41 67 49 4C 45 4E 54	e Ar	cquisition #
-640.1us 06 FF		Normal
99.92us 05 FF FF 02		5.0MSa/s
1.384ms 02 10 4D 53 4F FF FF FF FF FF		0.01913475
5.000ms 03 10 00 00 00 FF FF 6F 73 4F ◆ 6.980ms 06 FF		71
7.720ms 05 FF FF 02	1	Channels =
8.896ms 02 20 FF FF	DC	10.0:1
	DC	10.0:1
M0SI:06	DC	1.12:1
D <sub>9</sub> MOSI1	DC	1.12:1
D <sub>8</sub> MISO1		
	1	
S, MOSI (03 10 00 00 00 (06 ) 05 FF (02		
MISO FF FF 6F 73 4F FF 73 4F FF 02 FF		
Lister Menu		
🕤 Display 💽 Scroll 🔰 Zoom to 🔰 Undo 🚺 Options		
S <sub>1</sub> (SPI) Lister Selection Zoom 🛶		

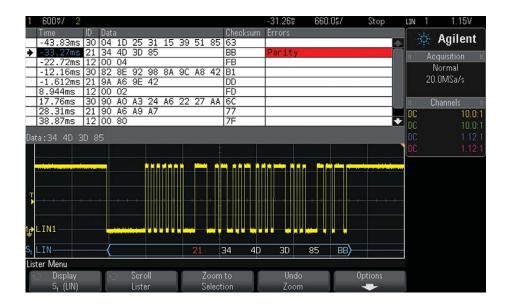
Tx and Rx input source	Analog channels 1, 2, 3, or 4 Digital channels D0 to D15		
Bus configuration	· · ·		
Baud rates	100 b/s up to 8 Mb/s		
Number of bits	5 to 9		
Parity	None, odd, or even		
Polarity	ldle low or idle high		
Bit order	LSB out first or MSB out first		
Triggering	Rx start bit		
	Rx stop bit		
	Rx data		
	Rx 1:data (9-bit format)		
	Rx 0:data (9-bit format)		
	Rx X:data (9-bit format)		
	Rx or Tx parity error		
	Tx start bit		
	Tx stop bit		
	Tx data		
	Tx 1:data (9-bit format)		
	Tx 0:data (9-bit format)		
	Tx X:data (9-bit format)		
	Burst (nth frame within burst defined by timeout)		
Hardware-based decode			
Number of decode traces	2 independent traces (Tx and Rx)		
Data format	Binary, hex, or ASCII-code characters		
Data byte display	White characters if no parity error, red characters if parity or bus error		
Idle bus trace	Mid-level bus trace in blue		
Active bus trace	Bi-level trace in blue		
Multi-bus analysis	RS232/UART plus one other serial bus (including another RS232/UART bus)		
Totalize/counter function	Total received frames		
	Total transmitted frames		
	Total parity error frames (with percentage)		



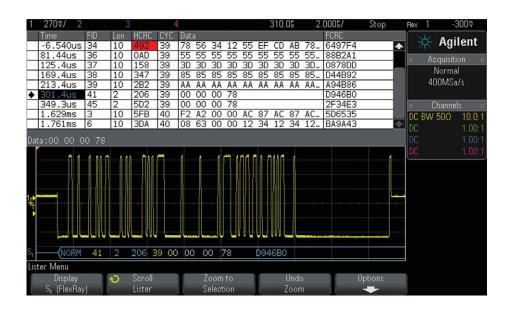
CAN input source	Analog channels 1, 2, 3, or 4		
-	Digital channels D0 to D15 (non-differential)		
Signal types	Rx		
	Tx		
	CAN_L CAN_H		
	Diff (L-H)		
	Diff (H-L)		
Baud rates	10 kb/s up to 5 Mb/s		
Triggering	Start-of-frame (SOF)		
	Remote frame ID (RMT)		
	Data frame ID (~RMT) Remote or data frame ID		
	Data frame ID and data		
	Error frame		
	All errors (includes protocol "form" errors that may not generate flagged error frames)		
	Acknowledge errors		
	Overload frames		
	ID length: 11 bits or 29 bits (extended)		
Hardware-based secode	Frame ID (hex digits in yellow)		
	Remote frame (RMT in green)		
	Data length code (DLC in blue) Data bytes (hex digits in white)		
	CRC (hex digits in blue = valid, hex digits in red = error)		
	Error frame (bi-level bus trace and ERR message in red)		
	Form error (bi-level bus trace and "?" in red)		
	Overload frame ("OVRLD" in blue)		
	ldle bus (mid-level bus trace in dark blue)		
	Active bus (bi-level bus trace in dark blue)		
Multi-bus analysis	CAN plus one other serial bus (including another CAN bus)		
Totalize function	Total frames, Total overload frames, Total error frames, Bus utilization (bus load)		
Eye-diagram Mask Testing (requires	various downloadable mask files available based on differential probing polarity, baud rate,		
DSOX3MASK)	and network length		



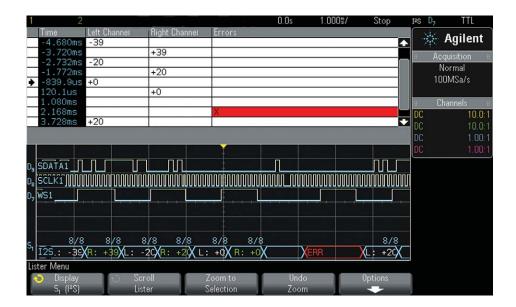
LIN specifications/characte	eristics (DSOX3AUTO)
LIN input source	Analog channels 1, 2, 3, or 4 Digital channels D0 to D15
LIN standards	LIN 1.3 or LIN 2.0
Baud rates	2400 b/s to 625 kb/s
Triggering	Sync break Frame ID (0X00 <sub>HEX</sub> to 0X3F <sub>HEX</sub> ) Frame ID and data
Hardware-based decode	Frame ID (6-bit hex digits in yellow) Frame ID and optional parity bits (8-bit hex digits in yellow if valid, red if parity bit error) Data bytes (hex digits in white) Lin 2.0 check sum (hex digits in white) Lin 1.3 check sum (hex digits in blue = valid, hex digits in red = error) Sync error ("SYNC" in red) THeader-max ("THM" in red) TFrame-max ("TFM" in red) Parity error ("PAR" in red) LIN 1.3 wake-up error ("WUP" in red) LIN 1.3 idle bus (mid-level bus trace in dark blue) LIN 2.0 idle bus (bi-level bus trace in dark blue) Active bus (bi-level bus trace in dark blue)
Multi-bus analysis	LIN plus one other serial bus (including another LIN bus)



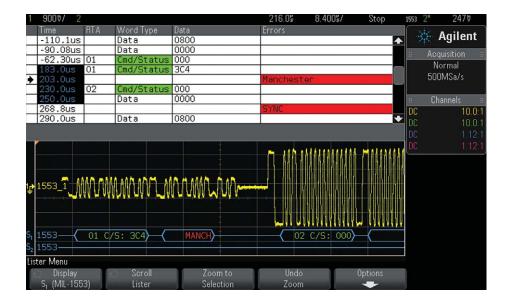
FlexRay input source	Channel 1, 2, 3, or 4 (using differential probe)		
FlexRay Channels	A or B		
Baud rates	2.5 Mbps, 5.0 Mbps, and 10 Mbps		
Frame triggering	<ul> <li>Frame type: startup (SUP), not startup (~SUP), sync (SYNC), not sync (~SYNC), null (NULL), not null (~NULL), normal (NORM), and All</li> <li>Frame ID: 1 to 2047 (decimal format), and All</li> <li>Cycle -     <ul> <li>Base: 0 to 63 (decimal format), and All</li> <li>Repetition: 1, 2, 4, 8, 16, 32, 64 (decimal format), and All</li> </ul> </li> </ul>		
Error triggering	<ul> <li>All errors</li> <li>Header CRC error</li> <li>Frame CRC error</li> </ul>		
Event Triggering	<ul> <li>Wake-up</li> <li>TSS (transmission start sequence)</li> <li>BSS (byte start sequence)</li> <li>FES/DTS (frame end or dynamic trailing sequence)</li> </ul>		
Frame decoding	<ul> <li>Frame type (NORM, SYNC, SUP, NULL in blue)</li> <li>Frame ID (decimal digits in yellow)</li> <li>Payload-length (decimal number of words in green)</li> <li>Header CRC (hex digits in blue if valid, or red digits if invalid)</li> <li>Cycle number (decimal digits in yellow)</li> <li>Data bytes (HEX digits in white)</li> <li>Frame CRC (hex digits in blue if valid, or red digits</li> </ul>		
Totalize function	<ul> <li>Total frames</li> <li>Total synchronization frames</li> <li>Total null frames</li> </ul>		
Eye-diagram Mask Testing (requires DSOX3MASK mask test option plus downloadable mask files)	TP1 standard voltage (10 Mbps only) TP1 increased voltage (10 Mbps only) TP11 standard voltage (10 Mbps only) TP11 increased voltage (10 Mbps only) TP4 10 Mbps, TP4 5 Mbps and TP4 2.5 Mbps		
Multi-bus Analysis	FlexRay plus one other serial bus (including another FlexRay bus)		



SCLK, WS, and SDATA input source	Analog channels 1, 2, 3, or 4		
	Digital channels D0 to D15		
Bus configuration:			
Transmitted word size	4 to 32 bits (user selectable)		
Decoded/Receiver word size	4 to 32 bits (user selectable)		
Alignment	Standard, left-justified, or right-justified		
Word select - low	Left-channel or right-channel		
SCLK slope	Rising edge or falling edge		
Decoded base	Hex (2's complement) or signed decimal		
Baud rates	2400 b/s to 625 kb/s		
Triggering:			
Audio channel	Audio left, audio right, or either		
Trigger modes	= (Equal to entered data value)		
	≠ (Not equal to entered data value)		
	< (Less than entered data value)		
	> (Greater than entered data value)		
	>< (Within range of entered data values)		
	<> (Out of range of entered data values)		
	Increasing value that crosses armed ( $<=$ ) and trigger (>=) entered data values		
	Decreasing value that crosses armed (>=) and trigger (<=) entered data values		
Hardware-based decode:			
Left channel	L: "decoded value" in white		
Right channel	R: "decoded value" in green		
Error	ERR in red (mismatch between transmitted and received word size, or invalid input signaling)		
Word size indicator	"# of TX / # of RX" CLKS in blue displayed above each decoded work		
	l <sup>2</sup> S plus one other serial bus ( <u>excluding</u> another l <sup>2</sup> S bus)		



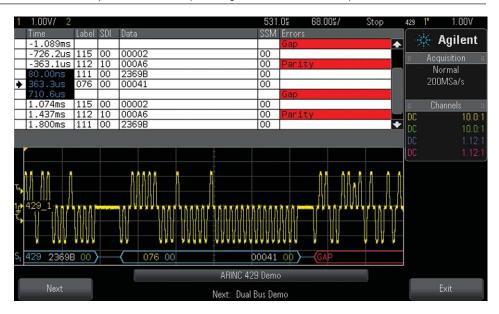
MIL-Std 1553 Input Source	Analog channels 1, 2, 3, or 4 (using a differential active probe)		
Triggering	• Data Word Start		
	Data Word Stop		
	Command/Status Word Start		
	Command/Status Word Stop		
	Remote Terminal Address (hex)		
	<ul> <li>Remote Terminal Address (hex) + 11 Bits (binary)</li> </ul>		
	Parity Error		
	Sync Error		
	Manchester Error		
Color-coded, hardware-accelerated • Base: HEX or Binary			
decode	<ul> <li>Command or Status Word ("C/S" in green)</li> </ul>		
	<ul> <li>Remote Terminal Address (hex or binary digits in green)</li> </ul>		
	<ul> <li>11 Bits following RTA (hex or binary digits in green)</li> </ul>		
	Data Word ("D" in white)		
	<ul> <li>Data Word Bits (hex or binary digits in white)</li> </ul>		
	<ul> <li>Parity Error (all decoded text in red)</li> </ul>		
	<ul> <li>Synchronization Error ("Sync" in red)</li> </ul>		
	Manchester Error ("Manch" in red)		
Eye-diagram Mask Testing (requires	System xfmr-coupled Input		
DSOX3MASK mask test option plus	System direct-coupled Input		
downloadable mask files)	BC xfmr-coupled Input		
	BC direct-coupled Input		
	RT xfmr-coupled Input		
	RT xfmr-coupled Input		
Multi-bus Analysis	MIL-STD 1553 plus one other serial bus, (including another MIL-STD 1553 bus)		
,			



High (100 kbps) Low (12.5 kbps) Word Start Word Stop
Label (octal) Label (octal) + Bits (binary) Label Range (octal) Parity Error Word Error Gap Error Word or Gap Error All Errors All Bits (useful for eye-diagram testing)) All 0 Bits All 1 Bits
Word Format: Label/SDI/Data/SSM or Label/Data/SSM or Label/Data Label (octal digits in yellow) SDI (binary digits in blue) Data (hex or binary digits in white) SSM (binary digits in green) Errors (text in red)
Total Words Total Errors
100 kbps Eye Test 100 kbps 1's Test 100 kbps 0's Test 100 kbps Null Test 12.5 kbps Eye Test 12.5 kbps 0's Test 12.5 kbps Null Test

Multi-bus Analysis

ARINC 429 plus one other bus (including another ARINC 429 bus)



#### **Ordering Information**

The various serial bus options are compatible on all models of the Agilent InfiniiVision 3000 X-Series oscilloscopes. Existing InfiniiVision 3000 X-Series oscilloscopes can also be upgraded with these options.

Model number	Description	
DSOX3EMBD	I <sup>2</sup> C and SPI trigger and decode	
DSOX3COMP	RS232/UART trigger and decode	
DSOX3AUTO	CAN and LIN trigger and decode	
DSOX3FLEX	FlexRay trigger and decode	
DSOX3AER0	MIL-STD 1553 and ARINC 429 trigger and decode	
DSOX3AUDIO	I <sup>2</sup> S trigger and decode	
DSOX3SGM	Segmented memory	
DSOX3MASK	Mask test option	
DSOX3ADMATH	Advanced waveform math	
DSOX3PWR	Power measurements	
DSOXDVM	Integrated 3-digit voltmeter	
DSOX3VID	HDTV triggering and analysis	
DSOX3MEMUP	Acquisition memory upgrade	
N2791A	25-MHz differential active probe (recommended for CAN, MIL-STD 1553, and ARINC 429 applications)	
N2792A	200-MHz differential active probe (recommended for FlexRay applications)	
N2793A	800-MHz differential active probe (recommended for FlexRay applications)	
0960-2926	DB9 probe head adapter for N2791A and N2792A	
0960-2927	DB9 probe head adapter for N2793	
•		

Additional options and accessories are available for Agilent's InfiniiVision 2000 and 3000 X-Series oscilloscopes. Refer to the *Agilent InfiniiVision Oscilloscope Probes and Accessories* selection guide or 2000 X-Series or 3000 X-Series data sheets for ordering information about these additional options and accessories.

#### **Related Agilent literature**

Publication Title	Publication Type	Publication Number
Agilent InfiniiVision 2000 X-Series Oscilloscopes	Data Sheet	5990-6618EN
Agilent InfiniiVision 3000 X-Series Oscilloscope	Data Sheet	5990-6619EN
Agilent Technologies InfiniiVision Series Oscilloscope Probes and Accessories	Selection Guide	5968-8153EN
Evaluating Oscilloscope Segmented Memory for Serial Bus Applications	Application Note	5990-5817EN
CAN Eye-diagram Mask Testing	Application Note	5991-0484EN
FlexRay Eye-diagram Mask Testing	Application Note	5990-4923EN
MIL-STD 1553 Eye-diagram Mask Testing	Application Note	5990-9324EN
ARINC 429 Eye-diagram Mask Testing	Application Note	5990-9325EN

To download these documents, insert the publication number in the URL: http://cp.literature.agilent.com/litweb/pdf/xxxx-xxxxEN.pdf

#### **Product Web site**

For the most up-to-date and complete application and product information, please visit our product Web site at: www.agilent.com/find/3000X-Series

#### www.agilent.com www.agilent.com/find/3000X-Series



www.agilent.com/find/emailupdates Get the latest information on the products and applications you select.

# LXI

#### www.lxistandard.org

LAN eXtensions for Instruments puts the power of Ethernet and the Web inside your test systems. Agilent is a founding member of the LXI consortium.



#### www.axiestandard.org

AdvancedTCA<sup>®</sup> Extensions for Instrumentation and Test (AXIe) is an open standard that extends the AdvancedTCA for general purpose and semiconductor test. Agilent is a founding member of the AXIe consortium.

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