

XT2640 Series

Precision Multi-Channel
Power Analyzer

DC: +0.000 RC+DC: +7.487 DC: 0.00 RC+DC: 13.83

+11.60 UAR +0.5422 PF
RC+DC: +11.63 RC+DC:+0.5412 PHS: +57.17

119.17 U
DC: +0.018 RC+DC:119.17 DC: +7.1m RC+DC: 116.1m Pk4:-169.9 Pk4:-0.412 Pk4:-0.387 CF:3.552

BC: +0.000 RC+DC: +7.487 DC: 0.00 RC+DC: 13.83 HARMONICS

UECTORS

HISTORY

SCOPE

SYS CON
CF: 1.427 CF: 3.552 CHNL C

Vitrek's XT2640 is three power analyzers in a single chassis with a single interface. The XT2640 may have up to 4 Channels installed or optionally with the MU option combine units to grow to thousands of channels, which may be any combination of channel cards and with any combination of available current input options.

Quality and Reliability

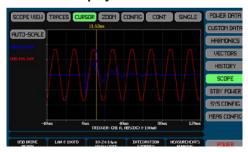
Vitrek, founded in 1990, is the premier source of precision power testing and measuring equipment for industrial and consumer product development and manufacturing. Vitrek's sophisticated technology provides companies the edge in design verification and product manufacturability.

25 Years Industry EXPERTISE

History Displays



Harmonics Displays with Limits



Standby Power



Vector Diagrams



Oscilloscope Displays



INDUSTRIES SERVED

- Automated Production Testing Ballast Testing Consumer Products Engineering Labs
- Instrument Maintenance & Repair Peak Power & Consumption Measurements
- Light Output Verification
 Product Compliance Testing
 Test Labs



XT2640 channels may be configured in any one (or none) of the 3 virtual power analyzers. Each virtual power analyzer may be configured for up to all channels installed. Each VPA is independently configured for multi-channel wiring configuration, signal filtering, default measurement coupling, display results smoothing and significant digits, VA/VAR combine method, and efficiency grouping. VPAs may optionally be configured to be synchronized to each other.



XVIEW Software

While all Vitrek precision test equipment is designed to be used in a completely stand-alone manner, there are times when external tools can aid or enhance the operation of an instrument.

XView software tools and drivers are designed to help easily configure an instrument from a single screen, or are used to view a complete set of measurements in a single screen.

Other XView tools are designed for data collection where results can be recorded in an Excelcompatible file for post-processing, insertion into reports, or simply for archival purposes.

Ordering Information									
822-XT2640AD (STD)	Basic Power Analyzer chassis, with 26A element, 220-260ksps, 24bit effective resolution, 0.025% rdg (V) (1 thru 4 Channel)	892-26GPIB	IEEE-488/GPIB interface option (note: this replaces the USB and Ethernet interfaces)						
822-XT2640WD	Basic Power Analyzer chassis, with 26W element, 850-1100ksps, 24bit effective resolution, 0.2% rdg, (V) (1 thru 4 Channel)	892-26xx OPT D (STD)	Standard current option, 2 ranges, 20Arms max						
822-XT2640SD	Basic Power Analyzer chassis, with 26S element, 220-260ksps, 22bit effective resolution, 0.1% rdg (V) (1 thru 4 Channel)	892-26xx OPT H	High current option, 1 range, 35Arms max						
OLE XIEG-100B	Basic Power Analyzer chassis, with 26E element, 220-260ksps,	892-26xx OPT X	External current option, 2 ranges, 15Vrms input max						
822-XT2640ED	24bit effective resolution, 0.05% rdg, (V), IEC 61000-3-2 compliant harmonic measurement. (1 thru 4 Channel)	892-26xx OPT H500	Allows up to 500 harmonics on all W cards in a unit						
	Power Analyzer A Element, 220-260ksps, 24bit effective	892-26xx OPT MU	Multi Unit Option						
822-26AD (Card)	resolution, 0.025% rdg (V)	892-26xx OPT EN	Built-in EN61000 compliance firmware for all cards						
822-26WD (Card)	Power Analyzer W Element, 850-1100ksps, 24bit effective resolution, 0.2% rdg, (V)		in a unit						
OZZ ZOWD (Odru)	Power Analyzer S Element, 220-260ksps, 22bit effective	892-HC-7	Hard carrying case, pelican-type						
822-26SD (Card)	resolution, 0.1% rdg (V)	892-RM-7	4U (7in height) rackmount kit for 4 channel units						
822-26ED (Card)	Power Analyzer E Element, 220-260ksps, 24bit effective resolution, 0.05% rdg, (V)	892-280x Cable IEC	Connection Cable						
522 2525 (Sala)		LS-XT2640	Lead Set						



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1 DIMENSIONAL, ENVIRONMENTAL AND POWER SUPPLY SPECIFICATIONS

1.1 DIMENSIONAL

Nominal Dimensions 137mmH x 248mmW x 284mmD (5.4" x 9.75" x 11.2") with feet not

Nominal Weight extended 3.2kg (7lb) net, 5kg (11lb) shipping

1.2 ENVIRONMENTAL

Storage Environment -20 to 75C (-4 to 167F) (non-condensing)

Operating Environment 0 to 40C (32 to 104F), <85% RH (non-condensing), Pollution

Operating Altitude Degree 2 0 to 2000m (6560ft) ASL

1.3 POWER SUPPLY

Line Power Installation Category II; 85-264Vrms, 45 to 65Hz, 40VA

max. Internally fused with a non-user serviceable fuse

2 ELECTRICAL CHANNEL INPUT AND ACCURACY SPECIFICATIONS

Note:

All percentages are % of reading unless otherwise described.

2.1 INPUT ISOLATION SPECIFICATIONS

Valid for any V terminal to XT2640 chassis ground; any A terminal to XT2640 chassis ground; and between any V and any A terminal.

Impedance $>1G\Omega \parallel <30pF$

Max. Voltage 4500V_{PK} max without damage

2500V_{RMS} max for <1s without damage

 $1000V_{\text{RMS}}$ max continuous rated working voltage (CAT I/ II) $600V_{\text{RMS}}$ max continuous rated working voltage (CAT III) $300V_{\text{RMS}}$ max continuous rated working voltage (CAT

2.2 VOLTAGE MEASUREMENT SPECIFICATIONS

The specifications for voltage are independent of the current input option installed in the respective channel.

2.2.1 VOLTAGE INPUT CAPABILITY AND CHARACTERISTICS

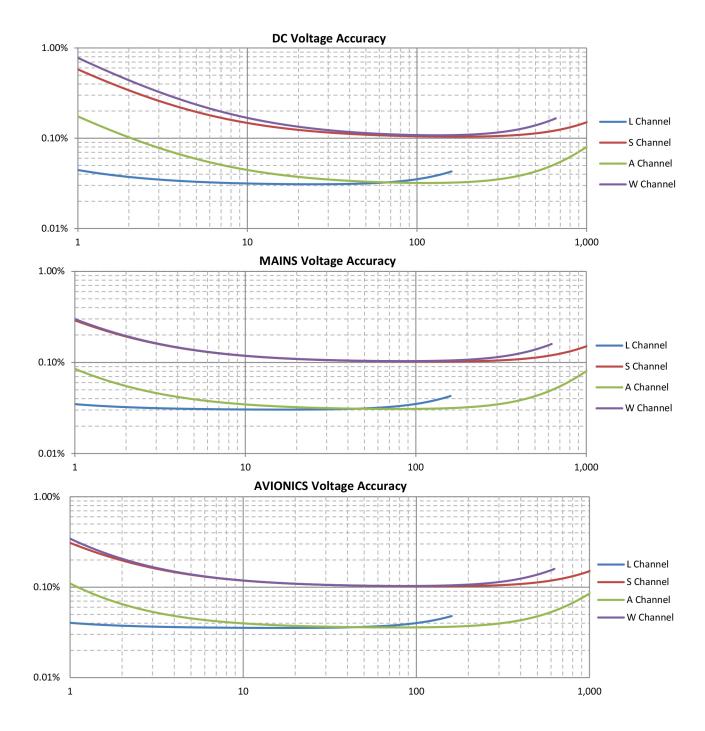
Specification		S Channel Type	A Channel Type	L Channel Type	W Channel Type
	<1ms	<3000V _R	_{MS} and V _{PK}	<500V _{RMS} and 3000V _{PK}	$<3000V_{RMS}$ and V_{PK}
N. B	<100ms	<200	0V _{RMS}	<300V _{RMS}	<1500V _{RMS}
No Damage	<5s	<1500V _{RMS}		<250V _{RMS}	<1000V _{RMS}
Voltage Range	Continuous	<1000V _{RMS}		<160V _{RMS}	<650V _{RMS}
	XT2640	As a		above	
Measurable Voltage Range	Unpowered	<1803V _R	_{MS} and V _{PK}	$<182.3V_{RMS}$ and V_{PK}	$<1803V_{RMS}$ and V_{PK}
Specified Voltage Range		<1000V _{RMS} 8	and <1750V _{PK}	$<160V_{RMS}$ and $<175V_{PK}$	$<650V_{RMS}$ and $<1750V_{PK}$
Impedance Burden		1.201MΩ	2 ± 0.25%	$121k\Omega \pm 0.25\%$	399.5 k $\Omega \pm 0.25\%$
3dB Bandwidth (typical)		900kHz			3MHz



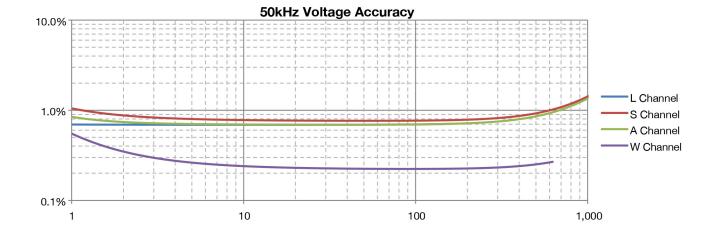


2.2.2 VOLTAGE MEASUREMENT ACCURACY

The charts below show guaranteed maximum voltage errors for DC, MAINS, AVIONICS, and 50kHz throughout a 1V to 1000V range of applied voltages expressed as % of reading and are valid within $\pm 5C$ of the calibration temperature (add 0.005% per C beyond this) and where no significant common-mode is present. Following the charts is a table which can be used to calculate the guaranteed accuracies for applications other than shown in the charts and also for the computation of numerical errors.







2.2.2.1 PRIMARY VOLTAGE MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in primary voltage measurements (e.g. DC, AC, AC+DC, Rectified, Peak, Valley, Peak-Valley).

			ly to all results as shown be	CALING ERRORS elow as a percentage of the one frequencies, apply to ear				
Spec	cification		S Channel Type	A Channel Type	L Channel Type	W Channel Type		
Base Scaling Error Apply to all results			0.1%	0.0	03%	0.1% (0.2% if 2ms LF/PERIOD)		
		AVIONICS	None	0.0	005%	None		
		LF or VLF		0.01%		0.05%		
Frequency Dependent Se	aalina Errar	<10kHz		F*0.005%		F*0.002%		
Apply to all results other than		10k-40kHz		0.05%+(F-10)*0.012%		1 0.00270		
ppry to an roomto outor than	50 0	40k-100kHz		0.41%+(F-40)*0.025%		0.08%+(F-40)*0.004%		
		100k-1MHz		Typically (F/1000)2*100%		0.32%+(F-100)*0.013%		
		>1MHz		Not specified		Typically (F/3500)2*100%		
Self-Heating Scaling Erro Apply to all results (only si voltages) 1 minute nomina	gnificant at high	er	0.05%*(V _{AC}	C+DC/1000) ²	0.5%*(V _{AC+DC} /1000) ²	0.15%*(V _{AC+DC} /1000) ²		
Temperature Scaling Err Apply to all results if outsic calibration temperature				0.005% per C outside of ±	5C from calibration tempera	ture		
Bandwidth Limit Scaling using USER bandwidth se			$10\%*(F/F_{BW})^2$, unspecified for $F > 0.3*F_{BW}$					
		Apply to all re	MAXIMUM FLosults as shown below in Vo	OOR ERRORS Its (generally only significar	nt at low input			
Spec	cification		S Channel Type	A Channel Type	L Channel Type	W Channel Type		
Base Floor Error Apply to all results			1.8mV	450μV	45μV	1.8mV		
DC Floor Error Apply to DC and RECTIFIE Apply to AC+DC results at		by V _{DC} /V _{AC+DC}	3mV	1mV	100μV	5mV		
AC Floor Error	MAINS, LF, VLF		100μV/V _{RDG}	100μV/V _{RDG}	4µV/V _{RDG}	200μV/V _{RDG}		
Apply to AC, AC+DC,	AVIONICS	& F _{BW} ≤50kHz	300µV/V _{RDG}	300µV/V _{RDG}	8µV/V _{RDG}	650μV/V _{RDG}		
and RECTIFIED results		Otherwise	1.1mV/V _{RDG}	1.1mV/V _{RDG}	11µV/V _{RDG}	1.5mV/V _{RDG}		
Peak Floor Error Apply	MAINS, LF, VLF	& F _{BW} ≤10kHz	40mV	40mV	8mV	60mV		
to PK, VLY and PK-VLY	AVIONICS	& F _{BW} ≤50kHz	75mV	75mV	11mV	125mV		
results		Otherwise	125mV	125mV	17mV	175mV		
Common Mode Error Apply to AC, AC+DC, and RECTIFIED results Apply using voltage on V LO terminal relative to chassis ground. Error has 90° phase shift to common-mode voltage					100nV per V.Hz (1.15mV@230V/50Hz)	700nV per V.Hz (8.05mV@230V/50Hz)		
Adjacent Channel Error Apply to AC, AC+DC, and RECTIFIED results Apply using adjacent channel A LO or V LO terminal voltage relative to chassis ground. Error has 90° phase shift to adjacent channel voltage			300nV p (3.45mV@2		30nV per V.Hz (345μV@230V/50Hz)	210nV per V.Hz (2.415mV@230V/50Hz)		



2.2.2.2 SECONDARY VOLTAGE MEASUREMENT ACCURACY TABLE

Specification		S Channel Type	A Channel Type	L Channel Type	W Channel Type
Crest Factor Error		(Total Floor Error from prece	ding table for PK results) / V	AC
Form Factor Error		(Tota	Floor Error from preceding	table for AC+DC results) / V _R	ECTIFIED
Inter-Channel Error For 120° between equal amplitudes		(Relevant Volta	age Errors from preceding ta	able at the inter-channel volta	age) + 0.0015%*F
		· ·	+ (H/N) ^{2*} 0	at V and F of the harmonic of .3% of reading the frequency of the harmonic	
Harmonic or Spectrum Error	<10kHz	0.01% of V _{AC+DC} 0.006% of V _{AC+DC}		0.015% of V _{AC+DC}	
	10k-115kHz			0.03% of V _{AC+DC}	
	115k-435kHz			0.08% of V _{AC+DC}	
Inter-Channel Fundamental Phase Err	or	0.02°+0.15°*F			0.01°+0.07°*F
Harmonic-Fundamental Phase Error (BANDWIDTH configured as UNFILTER		0.02°+0.1°*F+0.001°*H			0.02°+0.03°*F+0.001°*H
%THD Error		(0.005+0.000025*N)*%THD+0.00005*N*√N + from below using the frequency of highest included harmonic			
Errors shown are all expressed in %	<10kHz	0.025+1.25/V _{AC}	0.015+1/V _{AC}	0.015+0.2/	0.03+1.5/V _{AC}
THD units	10k-115kHz	V _{AC} 0.	15+3.5/V _{AC}	0.15	0.06+4/V _{AC}
	115k-435kHz	+0.35/V _{AC} Not Available			0.15+4/V _{AC}

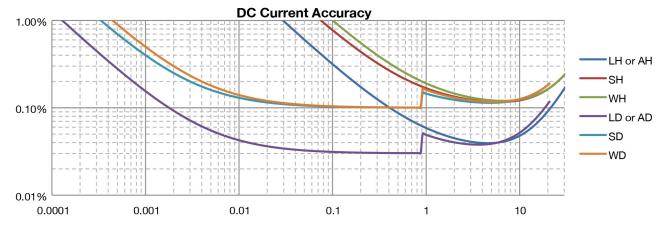
2.3 CURRENT MEASUREMENT SPECIFICATIONS

2.3.1 CURRENT INPUT CAPABILITY AND CHARACTERISTICS

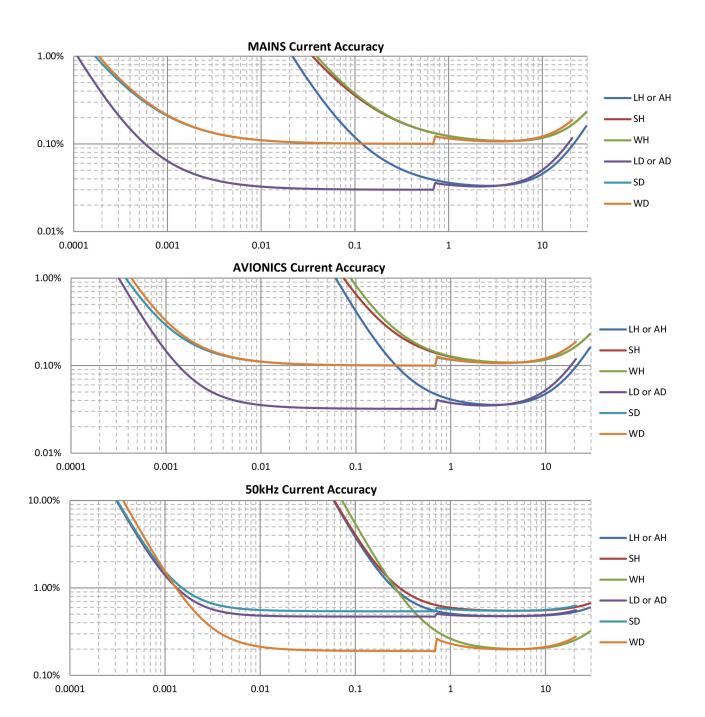
Spec	cification	Channel Type	Option H	Option D HI Range or Auto-Range when on HI Range	Option D LO Range or Auto-Range when on LO Range	Option X HI Range	Option X LO Range	
	<8ms	All	<200A _{RMS} and <300A _{PK}	<150A _{RMS} and <250A _{PK}	<60A _{RMS} and <150A _{PK}	<200V _{RMS} and <300V _{PK}	<20V _{RMS} and <30V _{PK}	
No Damage	<40ms	All	<75A _{RMS}	<50A _{RMS}	<40A _{RMS}	<50V _{RMS}	<10V _{RMS}	
Current Range	<1s		<50A _{RMS}	<30A _{RMS}	<5A _{RMS}	<30V _{RMS}	<5V _{RMS}	
	Continuous	All	<30A _{RMS}	<20A _{RMS}	<2A _{RMS}	<25V _{RMS} and V _{PK}	<5V _{RMS} and V _{PK}	
	XT2640 Unpowered	All	As Above	<2A _{RMS} and <150A _{PK}		<25V _{RMS} ar	nd <300V _{PK}	
Measu	ırable Current Range	All	<225ARMS and APK	<150ARMS and APK	<1.02A _{RMS} and A _{PK}	<23.1V _{RMS} and V _{PK}	<0.576V _{RMS} and V _{PK}	
Specified Current	t Range	All	<30A _{RMS} and <200A _{PK}	<20A _{RMS} and <140A _{PK}	<1A _{RMS} and A _{PK}	<15V _{RMS} and <20V _{PK}	<0.55V _{RMS} and V _{PK}	
Impedance Burde	Impedance Burden		$2.5 m\Omega$ to $7 m\Omega$	$4m\Omega$ to $12m\Omega$	$0.562\Omega \pm 0.75\%$	20.5 k $\Omega \pm 0.25$ %	10.25 k $\Omega \pm 0.25$ %	
OdD Donahusidah /	0-ID D				1.25MHz			
3dB Bandwidth (t	ypicaij	W		5MHz		3MHz		

2.3.2.CURRENT MEASUREMENT ACCURACY

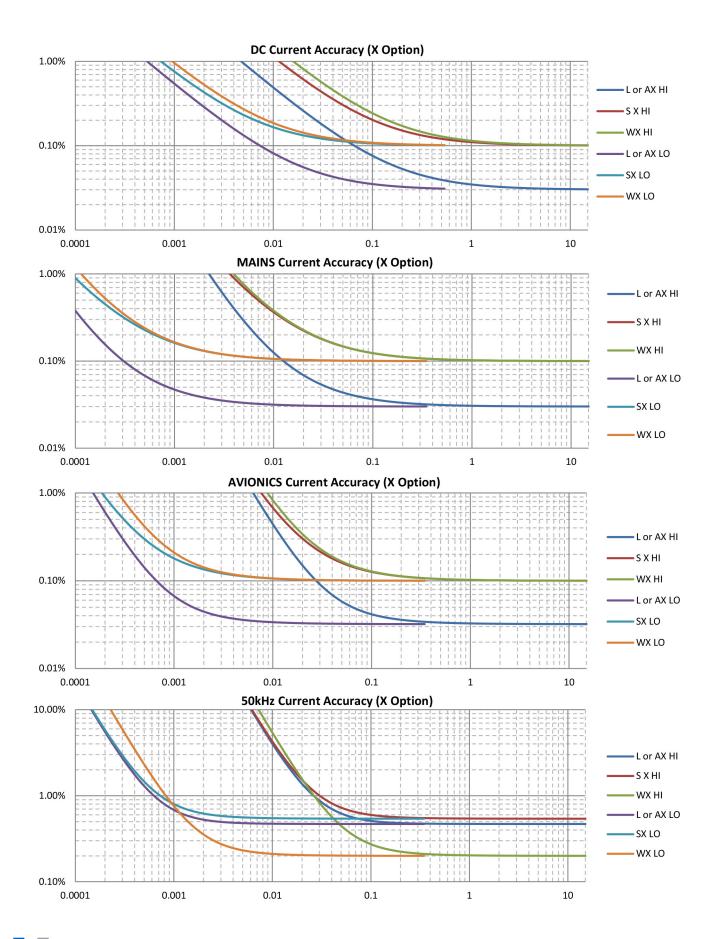
The charts below show guaranteed maximum current errors for DC, MAINS, AVIONICS, and 50kHz throughout a $100\mu A$ to 30Arange of applied currents expressed as % of reading and are valid within $\pm 5C$ of the calibration temperature (add 0.005% per C beyond this) and where no significant common-mode is present. Following the charts is a table which can be used to calculate













2.3.2.1 PRIMARY CURRENT MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in primary current measurements (e.g. DC, AC, AC+DC, Rectified, Peak, Valley, Peak-Valley).

			MA	XIMUM SCALING E	RRORS						
		If			ercentage of the reading quencies, apply to each l	evel &					
Specific	cation	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range				
Base Scaling Erro	or	A or L			0.03%						
Apply to all results	s	S or W		0.	1% (0.2% if 2ms LF/PER	IOD)					
	LF or VLF	S, A or L		0.01%							
LF Or VLF		W			0.05%						
	AVIONICS	A or L			0.002%						
Frequency		S or W S, A or L			None F*0.003%						
Dependent Scalin	ng <10kHz	W W			F*0.003%						
Error		S, A or L			0.03%+(F-10)*0.007%						
Apply to all results other than DC or	10k-40kHz	W			F*0.0015%						
MAINS	401 400111	S, A or L			0.24%+(F-40)*0.02%						
WIV WING	40k-100kHz	W		0.06%+(F-40)*0.003%	,	0.06%+(F-4	0)*0.004%				
	100k-1MHz	S, A or L			Typically (F/1250)2*100%)					
	TOUK-TIVIEZ	W		0.24%+(F-100)*0.0129	6	0.3%+(F-10	0)*0.015%				
	>1MHz	W	٦	Typically (F/5000)2*100%	ó	Typically (F/30	000)2*100%				
Self-Heating Sca Apply to all results significant at higher minute nominal tin	s (only er currents) 3	All	0.00015%*A _{AC+DC} 2	0.0002%*A _{AC+DC} 2		None					
Temperature Sca Apply to all results ±5C from calibration	s if outside of	All		0.005% per C	outside of ±5C from calib	oration temperature					
Bandwidth Limit Error Apply if usin bandwidth setting	ng USER	All		10%*(F/F _{BW})², unspecified above 0.3*F _{BW}							
		Apı		AXIMUM FLOOR EF on below in Amps (gener	RORS rally only significant at lo	w input					
Specific	cation	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range				
Base Floor Error		A or L	56µA	38µA	250nA	6μV	150nV				
Apply to all results	3	S or W	225μΑ	150μΑ	1μΑ	23µV	600nV				
DC Floor Error		A or L	0.23mA	0.15mA	1µA	40μV	5μV				
Apply to DC and		S	0.45mA	0.3mA	2μΑ	80μV	6μV				
RECTIFIED results Apply to AC+DC rafter multiplying by	esults	W	0.68mA	0.45mA	ЗµА	120µV	8μV				
M	IAINS, LF, VLF &	S, A or L	3.3µA/A _{RDG}	1.5µA/A _{RDG}	90pA/A _{RDG}	35nV/A _{RDG}	20pV/A _{RDG}				
AC Floor Error	F _{BW} ≤10kHz	W	5μA/A _{RDG}	2.5µA/A _{RDG}	125pA/A _{RDG}	50nV/A _{RDG}	50pV/A _{RDG}				
Apply to AC, AC+DC, and	AVIONICS &	S, A or L	33µA/A _{RDG}	15μΑ/A _{RDG}	0.9nA/A _{RDG}	350nV/A _{RDG}	200pV/A _{RDG}				
RECTIFIED _	F _{BW} ≤50kHz	W	50μA/A _{RDG}	25µA/A _{RDG}	1.25nA/A _{RDG}	500nV/A _{RDG}	500pV/A _{RDG}				
results	Otherwise	S, A or L	330µA/A _{RDG}	150μΑ/A _{RDG}	9nA/A _{RDG}	3.5µV/A _{RDG}	2nV/A _{RDG}				
ļĻ		W	500μA/A _{RDG}	250µA/A _{RDG}	12.5nA/A _{RDG}	5μV/A _{RDG}	5nV/A _{RDG}				
Peak Floor	IAINS, LF, VLF &	S, A or L	8mA	5mA	40μA	0.75mV	25µV				
Error	F _{BW} ≤10kHz	W	10mA	6.5mA	50μA	0.9mV	30µV				
Apply to PK,	AVIONICS &			17mA	125μA	2.5mV	65µV				
VLY and PK-	F _{BW} ≤50kHz	W S A or I	30mA 75mA	20mA 50mA	150µA	3mV	80µV				
VLY results	Otherwise	S, A or L W	90mA	60mA	400μA 500μA	7.5mV 10mV	200μV 250μV				
Common Mode E	Frror	۷V	JUIIA	OUTIA	σουμΑ	TOTTIV	200μν				
Apply to all results Apply using voltage on A LO terminal relative to chassis ground. Error has 90° phase shift to common-mode voltage		All	500pA per V.Hz	400pA per V.Hz	20pA per V.Hz	15nV per V.Hz	0.5nV per V.Hz				
ground. Error has	o chassis s 90° phase	All	(5.75µA@230V/50Hz)	(4.6μA@230V/50Hz)	(0.23µA@230V/50Hz)	(0.172mV@230V/50Hz)	(5.75μV@230V/50Hz)				

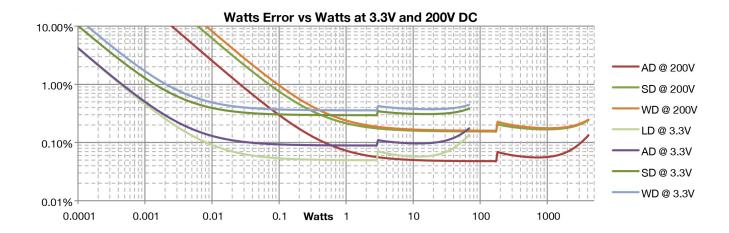


2.3.2.2 SECONDARY CURRENT MEASUREMENT ACCURACY TABLE

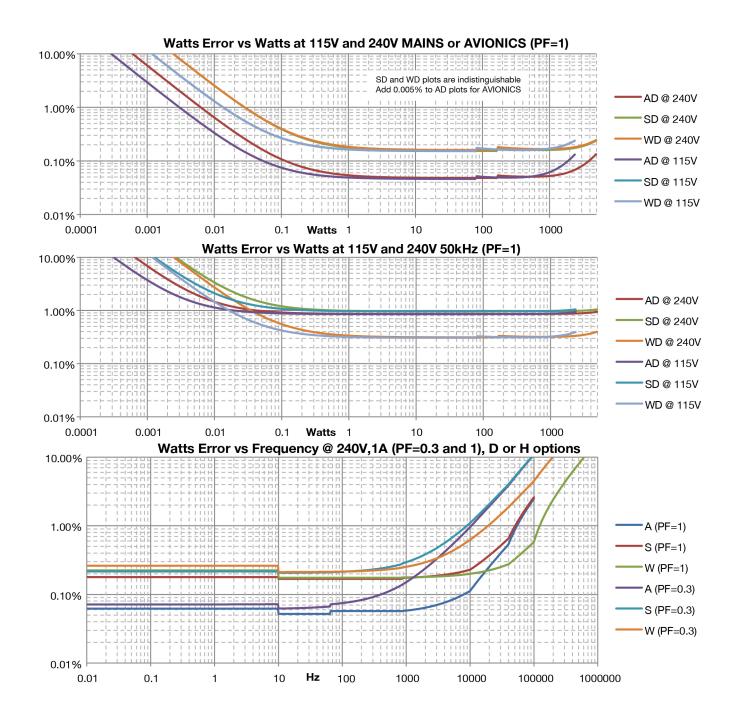
Specific	cation	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range			
Crest Factor Erro	or	All		(Total Current Floor Error from preceding table for PK results) / A _{AC}						
Form Factor Erro	or	All	(Total Current Floor Error	from preceding table f	or AC+DC results) / AREC	TIFIED			
Multi-Channel	A _N (2ø3w)	All		+ Relevant C	rent Errors from preced Current Errors from prec $_{\rm B}$ + 0.0005% of (A_{oA} + A_{oB}	ceding table for				
Error For similar current level and	A _{øC} (3ø3w 2ch)	All		+ Relevant C	rent Errors from preced Current Errors from prec B + 0.0015% of $(A_{oA} + A_{oB})$	ceding table for				
phase in each phase.	A _N (3ø4w)	All		+ Relevant C A _{oB} + Releva for A _{oC}	rent Errors from preced Current Errors from prec nt Current Errors from μ + 0.0015% of ($A_{oA} + A_{o}$	ceding table for preceding table $_{B}+A_{\circ C})^{*}F$				
		All			+ (H/N)2*0.3% of rea	of the harmonic or spec ading ncy of the harmonic or sp	·			
Harmonic or	<10kHz 10k-115kHz	A or L		0.006% of A _{AC+DC} 0.05% of A _{AC+DC}						
Spectrum Error	<10kHz 10k-115kHz	S	0.01% of A _{AC+DC} 0.05% of A _{AC+DC}							
	<10kHz 10k-115kHz	W	0.015% of A _{AC+DC} 0.03% of A _{AC+DC}							
	115k-435kHz				0.08% of A _{AC+DC}					
Current-Voltage		S, A or L			0.005° + 0.015°*F					
Fundamental Pha	ase Error	W	0.005° + 0.007° F							
Harmonic-Funda	mental Phase	S, A or L		0.02°+0.1°*F+0.001°*H						
Error (typical, BA configured as UN		W			0.02°+0.03°*F+0.001°	*H				
_	·	All		(0.005- + from below usi	+0.000025*N)*%THD+0 ng the frequency of hig).00005*N*√N hest included harmonic				
0/ TUD F	<10kHz	A or L	0.015+0.2/A _{AC}	0.015+0.15/A _{AC}	0.015+0.001/A _{AC}	0.015+0.025/A _{AC}	0.015+0.0006/A _{AC}			
%THD Error Errors shown are	10k-115kHz	AUL	0.15+2/A _{AC}	0.15+1.5/A _{AC}	0.15+0.01/A _{AC}	0.15+0.25/A _{AC}	0.15+0.006/A _{AC}			
all expressed in	<10kHz	S	0.025+0.2/A _{AC}	0.025+0.15/A _{AC}	0.025+0.001/A _{AC}	0.025+0.025/A _{AC}	0.025+0.0006/A _{AC}			
%THD units.	10k-115kHz	3	0.15+2/A _{AC}	0.15+1.5/A _{AC}	0.15+0.01/A _{AC}	0.15+0.25/A _{AC}	0.15+0.006/A _{AC}			
70 11 ID UIIILS.	<10kHz		0.03+0.25/A _{AC}	0.03+0.18/A _{AC}	0.03+0.0012/A _{AC}	0.03+0.03/A _{AC}	0.03+0.001/A _{AC}			
	10k-115kHz	W	0.06+2.5/A _{AC}	0.06+1.8/A _{AC}	0.06+0.012/A _{AC}	0.06+0.3/A _{AC}	0.06+0.01/A _{AC}			
	115k-435kHz		0.15+2.5/A _{AC}	0.15+1.8/A _{AC}	0.15+0.012/A _{AC}	0.15+0.3/A _{AC}	0.15+0.01/A _{AC}			

2.4 WATTS, VAR AND VA MEASUREMENT SPECIFICATIONS

The charts below show guaranteed maximum Watts errors for DC, MAINS, AVIONICS, and 50 kHz from $100 \mu\text{W}$ up to the highest available using a D option current measurement (H and X option current accuracies are similar within their respective range of currents and are not shown for clarity), expressed as % of Watts reading and are valid within $\pm 5\text{C}$ of the calibration temperature (add 0.005% per C beyond this) and where no significant common-mode is present. Following the charts is a table which can be used to calculate the guaranteed accuracies for applications other than shown in the charts and also for the computation of









2.4.1 WATTS, VAR AND VA MEASUREMENT SPECIFICATIONS 2.4.1.1 PRIMARY WATTS, VAR AND VA MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in all Watts, VA and VAR measurements except harmonic Watts.

Note that by definition DC Watts and DC VA are identical, and DC VAR is zero

			MAXIM	UM SCALING ERR	ORS		
		A If signal	pply to all results as sh	nown below as a perceivels at multiple frequen	ntage of the reading	el &	
Specification	Channel Type	Option H	Option D HI Range	Option D LO Range		Option X LO Range	
Base Scaling Error		A or L			0.045%		
Apply to all results		S or W		0.15	5% (0.3% if 2ms LF/PE	RIOD)	
	LF or VLF	S, A or L			0.01%		
	21 01 121	W			0.05%		
	AVIONICS	A or L S or W			0.005% None		
Cuantana y Danandant		S, A or L			F*0.006%		
Frequency Dependent Scaling Error	<10kHz	W			F*0.0025%		
Apply to AC component	10k-40kHz	S, A or L			0.06%+(F-10)*0.014%	Ó	
of all results other than	10K-40K112	W			F*0.0025%		
DC or MAINS	40k-100kHz	S, A or L			0.48%+(F-40)*0.032%		to 00550/
		W S A or I	(0.1%+(F-40)*0.005%	Typically (F/1100)2*1509	0.1%+(F-40)	0.0055%
	100k-1MHz	S, A or L W	0	.4%+(F-100)*0.018%	Typically (171100) 130	0.43%+(F-10)n)*n n2%
	>1MHz	W		pically (F/5000)2*150%		Typically (F/30)	
Self-Heating Scaling Error			. , ,	, , , , , , , , , , , , , , , , , , ,		. , p. ea , (1 / e e	20, 100,0
Apply as % of Power reading results using voltage and content of the street of the str	current Self- us tables	All		Add Volta	ge and Current Self-He	eating Errors	
Temperature Scaling Error Apply to all results if outsic from calibration temperatu	le of ±5C ire	All		0.005% per C o	utside of ±5C from cali	bration temperature	
Bandwidth Limit Scaling End AC component of all resusing USER bandwidth set	sults if	All	20%*(F/F _{BW})², unspecified above 0.3*F _{BW}				
		esults as		IUM FLOOR ERRO VA or VAR as applicab		ficant at low input	
Specification	1	Channel Type	Option H Option D HI Range Option D LO Range Option X HI Range Option X LO				Option X LO Range
		L	(V _{AC+DC} *56μA) + (A _{AC+DC} *45μV)	(V _{AC+DC} *38μA) + (A _{AC+DC} *45μV)	(V _{AC+DC} *250nA) + (A _{AC+DC} *45μV)	(V _{AC+DC} *6μA) + (A _{AC+DC} *45μV)	(V _{AC+DC} *0.15μA) + (A _{AC+DC} *45μV)
Base Floor Error Apply to all results		А	(V _{AC+DC} *56μA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *38μA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *250nA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *6μA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *0.15μA) + (A _{AC+DC} *450μV)
		S or W	(V _{AC+DC} *225μA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *150μA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *1µA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *23μA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *0.6µA) + (A _{AC+DC} *1.8mV)
DC Floor Error Apply to DC and AC+DC ro the Voltage and Current Do Errors from previous tables	C Floor	All	(V _{DC} *Current DC Floor Error) + (A _{DC} *Voltage DC Floor Error) + (Current DC Floor Error*Voltage DC Floor Error				
AC Floor Error (VA and VAI to AC and AC+DC VA & V/ using voltage and current / Errors from previous tables	AR results AC Floor	All	(V _{AC} *Current AC Floor Error) + (A _{AC} *Voltage AC Floor Error)				
Common Mode Error (VA a Apply to AC component of results using the Voltage a Common Mode Errors fron tables.	All	(V _{AC} *Current Common Mode Error) + (A _{AC} *Voltage Common Mode Error)					
Common Mode Error (Watts only) Apply to AC component of Watts results using the Voltage Common Mode Error from previous table			(A _{AC} *Voltage Common Mode Error)				
Adjacent Channel Error Apply to AC component of using the Voltage and Curr Channel Errors from previo	rent Adjacent	All	(VA	•	, , ,	age Adjacent Channel I	Error)
Phase Floor Error (Watts o		S, A or L	А	lternately, as a worst c		expressed as F*0.028%	of VA
AC and AC+DC Watts resu	JITS	W	A		PF _{FUND} - cos(cos-1(PF _{FUI} ase (at PF=0) this can	чр) + 0.007°*F)) expressed as F*0.013%	of VA



2.4.1.2 HARMONIC WATTS MEASUREMENT ACCURACY TABLE

Specification Change Type			Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range		
		All	AC Watts Errors of	AC Watts Errors other than Phase Floor Error from preceding table at levels and F of the harmonic or spectrum point + (H/N)2*0.5% of reading + from below using the frequency of the harmonic or spectrum point					
Harmonic or	<10kHz	A or L	0.006% + (0.004%+0.028%*F)/PF						
Spectrum Watts	10k-115kHz	AUL	0.05% + (0.004%+0.028%*F)/PF						
Error	<10kHz	S		0.01% + (0.004%+0.028%*F)/PF					
	10k-115kHz	3		0.05	5% + (0.004%+0.028%	*F)/PF			
	<10kHz			0.01	5% + (0.004%+0.013%	6*F)/PF			
	10k-115kHz	W		0.03% + (0.004%+0.013%*F)/PF					
	115k-435kHz			0.08	3% + (0.004%+0.013%	*F)/PF	`		

2.5.1 PF MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in PF measurements. For PF_{FUND} apply only the Base Floor and Phase Errors.

Note: DC PF is 1.0 by definition and has no error; the table below applies to AC, AC+DC and FUND PF results.

Specification	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range	
	L	(56μΑ/A _{AC+DC}) + (45μV/V _{AC+DC})	(38μΑ/A _{AC+DC}) + (45μV/V _{AC+DC})	(250nA/A _{AC+DC}) + (45µV/V _{AC+DC})	(6μΑ/A _{AC+DC}) + (45μV/V _{AC+DC})	(0.15μΑ/Α _{AC+DC}) + (45μV/V _{AC+DC})	
Base Floor Error Apply to all PF	А	(56μΑ/A _{AC+DC}) + (450μV/V _{AC+DC})	(38µA/A _{AC+DC}) + (450µV/V _{AC+DC})	(250nA/A _{AC+DC}) + (450µV/V _{AC+DC})	(6μΑ/Α _{AC+DC}) + (450μV/V _{AC+DC})	(0.15µA/A _{AC+DC}) + (450µV/V _{AC+DC})	
results	S or W	(225µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	(150µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	(1µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	$(23\mu\text{A/A}_{AC+DC})$ + (1.8mV/V_{AC+DC})	(0.6µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	
AC Floor Error Apply to all PF results using voltage and current AC Floor Error from previous tables, this error always causes a reduced PF reading	All		-PF _{RDG} *((Current AC Floor Error/A _{RDG}) + (Voltage AC Floor Error/V _{RDG}))				
	L	$(0.23\text{mA/A}_{AC+DC}) + (0.1\text{mV/V}_{AC+DC})$	$(0.15\text{mA/A}_{AC+DC}) + (0.1\text{mV/V}_{AC+DC})$	$(1\mu A/A_{AC+DC}) + (0.1mV/V_{AC+DC})$	$(40\mu A/A_{AC+DC}) + (0.1mV/V_{AC+DC})$	(5μA/A _{AC+DC}) + (0.1mV/V _{AC+DC})	
DC Floor Error Apply to AC+DC PF result	А	$(0.23\text{mA/A}_{AC+DC}) + (1\text{mV/V}_{AC+DC})$	$(0.15\text{mA/A}_{AC+DC}) + (1\text{mV/V}_{AC+DC})$	$(1\mu A/A_{AC+DC}) + (1mV/V_{AC+DC})$	$(40\mu\text{A/A}_{AC+DC})$ + (1mV/V_{AC+DC})	(5μΑ/Α _{AC+DC}) + (1mV/V _{AC+DC})	
after multiplying by (1-PF)	S	$(0.45\text{mA/A}_{AC+DC}) + (3\text{mV/V}_{AC+DC})$	$(0.3\text{mA/A}_{AC+DC}) + (3\text{mV/V}_{AC+DC})$	$(2\mu A/A_{AC+DC}) + (3mV/V_{AC+DC})$	$(80\mu A/A_{AC+DC}) + (3mV/V_{AC+DC})$	(6μΑ/A _{AC+DC}) + (3mV/V _{AC+DC})	
	W	(0.68mA/A _{AC+DC}) + (5mV/V _{AC+DC})	$(0.45\text{mA/A}_{AC+DC}) + (5\text{mV/V}_{AC+DC})$	$(3\mu A/A_{AC+DC})$ + $(5mV/V_{AC+DC})$	$(120\mu A/A_{AC+DC}) + (5mV/V_{AC+DC})$	(8µA/A _{AC+DC}) + (5mV/V _{AC+DC})	
Phase Error	S, A or L		,	_{IND} - cos(cos ⁻¹ (PF _{FUND}) = st case (at PF=0) this ca	"	0028	
Apply to all PF results	W			IND - cos(cos ⁻¹ (PF _{FUND}) = st case (at PF=0) this ca		0013	



FREQUENCY MEASUREMENT SPECIFICATIONS 2.6

Frequency Range FUND setting of MAINS: 45Hz to 65Hz

FUND setting of AVIONICS: 300Hz to 900Hz

Otherwise-

LF/PERIOD setting of VLF: 0.0099Hz to 65Hz LF/PERIOD setting of LF: 0.19Hz to 1kHz

LF/PERIOD setting of 300ms period: 9Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 100ms period: 19Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 20ms period: 44Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 10ms period: 145Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) If BANDWIDTH set to USER setting then upper limit is 0.5*setting

DC Level DC offset is automatically eliminated

Min. Input (typical) Voltage: 0.5Vrms (W, S or A channel type) or 75mVrms (L channel type) at

fundamental Current, H option: 0.05Arms at fundamental

Current, D option: 0.04Arms (HI range) or 0.3mArms (LO range) at fundamental Current, X option: 5mVrms (HI range) or 150µVrms (LO range) at fundamental

Min. Pulse Width (typical) Greater of -

> 1.25µs (W channel type) or 5µs (other channel types) 0.001% of measurement period

10% of signal period

As shown below for FREQ SPEED settings of FAST/NORMAL/SLOW respectively -Update Period (nominal)

> LF/PERIOD setting of VLF: greater of 1/2/15s or 1 cycle LF/ PERIOD setting of LF: greater of 1/1/5s or 1 cycle LF/ PERIOD setting of 300ms period: 0.25s/0.75s/2s LF/PERIOD setting of 100ms period: 55ms/250ms/1s LF/PERIOD setting of 20ms period: 25ms/200ms/700ms LF/PERIOD setting of 10ms period: 10ms/100ms/300ms LF/PERIOD setting of

2ms period: 2ms/50ms/150ms

Resolution W Channel Type: 0.000125%/Update Period in seconds

Otherwise: 0.0005%/Update Period in seconds

(nominal) Maximum 0.01% + Resolution

Greater of (x2 if significant DC content) -Settling Time (nominal)

Error

b) 2 frequency measurement periods

a) 2 amplitude periods c) 4 cycles of the signal

MECHANICAL CHANNEL INPUT AND ACCURACY SPECIFICATIONS

(MT TYPE)

INPUT CAPABILITIES AND CHARACTERISTICS 3.1

SPD (Speed): BNC (isolated from XT2640 chassis), configurable as analog or digital Input Terminals

> input TRQ (Torque): BNC (isolated from XT2640 chassis), configurable as analog or digital input DIR (Direction): BNC (isolated from XT2640 chassis), digital input

Up to -15Vpk to +15Vpk specified Input Common-Mode

Up to -30Vpk to +30Vpk with no damage

Up to -12Vdc to +12Vdc specified Analog Input Range

Up to -15Vpk to +15Vpk specified

Up to -30Vpk to +30Vpk with no damage

LO: <0.8V (nominal) Digital Input Range HI: >2V (nominal)

Up to -30Vpk to +30Vpk with no damage

Input Impedance Each input nominally 150kΩ to XT2640 chassis ground

3.2 DIGITAL INPUT MEASUREMENT SPECIFICATIONS

Digital Frequency Timing Signal must be LO for >500ns

Signal must be HI for >500ns

Frequency measurement up to 500kHz (typically 900kHz)

Minimum measurable frequency limited by user set measurement period

DIR Setup/Hold Timing DIR must be stable for >550ns prior to and after active edge of SPD input

Maximum Frequency Error Measurement Period >10ms: 0.01%

Measurement Period ≤10ms: 0.015% Please visit www.vitrek.com for ordering information.



3.3 ANALOG INPUT MEASUREMENT SPECIFICATIONS

Maximum Input Error 0.05% + 1mV

Add (0.005% + 50µV) per C outside of ±5C from calibration temperature

4 ANALYSIS SPECIFICATIONS

4.1 INTEGRATION SPECIFICATIONS

Start Delay Time Zero to 99 days, 99 hours, 99 minutes, 99 seconds (1 second

resolution) 0.01% + 8ms maximum error

Integration Time Manual (unrestricted period of time), or 1 second to 99 days, 99 hours, 99 minutes, 99

seconds 0.01% + 1ms maximum error

Maximum Data (0.01% + 1ms) (not for integrated average data) + (0.03/measurement period in seconds)% per year error

4.2 HARMONIC ANALYSIS SPECIFICATIONS

Method DFT performed at each frequency on same set of sampled signals (there is no discontinuity

throughout the analysed frequency range)

Window Maximum F > (2/measurement period): Hann (also called

Hanning) Otherwise: Rectangular

Harmonic The smaller of -

a) A frequency of 435kHz (W type channels) or 115kHz (otherwise)

b) 500th (harmonics over the 100th requires option H500)

c) HARMONICS setting

d) If BANDWIDTH set to USER: 0.5*setting/fundamental frequency

Harmonic Bandwidth Nominally the greater of-

a) The smaller of fundamental frequency or 2/(LF/PERIOD measurement period)

b) If FUND set to AVIONICS: 20Hz

c) (Fundamental Frequency*Maximum Harmonic/2250)

Measurement Nominally (1/Harmonic Bandwidth)

Period Update Nominally the greater of -

Interval a) LF/PERIOD measurement period

b) Harmonic Measurement Period (from above)

c) 0.25ms x Σ(Maximum Harmonic for each channel configured for harmonics)

Data Available Volts, Amps and Watts amplitudes for each configured harmonic

Volts and Amps as a percentage of the fundamental of the same signal Volts and Amps THD as a percentage of the fundamental of the same signal Volts and Amps THD as a percentage of the AC+DC amplitude of the same signal

V and A Phase of fundamental relative to the voltage fundamental of the lowest numbered channel

in the VPA

V and A Phase of each non-fundamental harmonic relative to the fundamental of the same signal

Accuracy See relevant Voltage, Current and Watts accuracy specifications

4.3 SPECTRUM ANALYSIS SPECIFICATIONS

Method DFT performed at each frequency on same set of sampled signals (there is no discontinuity

throughout the analysed frequency range)

Window Hann (also called Hanning)

Frequency 0.01Hz to 1kHz

Resolution Nominally (1/ Frequency Resolution)

Measurement Period Minimum is 100 x Frequency Resolution

Maximum Frequency Maximum is the lowest of nominally -

a) 16384 x Frequency Resolution (under some circumstances as low as 8192 x Frequency Resolution)

b) 435kHz (W type channels) or 115kHz (otherwise)

Data Available Volts, Amps and Watts amplitudes for each configured spectrum

Accuracy frequency See relevant Voltage, Current and Watts accuracy specifications

4.4 CYCLE VIEW SPECIFICATIONS

Signal Range As specifications for Voltage and Current



Cycle Period Time From 2.3us (W type channels), 8.7us (otherwise) up to 100 seconds

Resolution Method 1/512th of a cycle

Maximum Error Mean cycle formed by asynchronously sampling all cycles within measurement period

As Voltage and Current Specifications for PK data (Watts = multiplication of V and A waveforms)

4.5 SCOPE SPECIFICATIONS

Signal Range As specifications for Voltage and Current Timebase 1/2/5 settings from 5us/div to 20s/div

Capture Depth Up to 32k points per signal

Capture Resolution <0.00005% of specified maximum measurable peak Voltage or Current

Sampling Period (nominal) Greater of -

1.1µs (W type channels) or 4.1µs (otherwise) 0.03% of timebase setting

Maximum Error As Voltage and Current Specifications for PK data (Watts = multiplication of V and A waveforms)

4.6 HISTORICAL DATA COLLECTION SPECIFICATIONS

Collection Time

Automatically continuously variable between 1 measurement period and 584.5 million years (collection is

automatically stopped after this time has elapsed but this is untested at the time of writing)

Time Note: this is the resolution by which you can determine when an event occurred, not that of the

XT2640 detecting events. All events are captured.

Resolution The greater of-

a) 1 pixel of displayed data (front panel) or 1 increment of the requested time interval (interface)

b) 1 measurement period of the data being recorded

c) A maximum of 1/4096th of the elapsed historical data collection time (typically 1/8192th).

Data Capture Every measurement is included in the maximum, average and minimum data for each increment of the

time resolution interval regardless of the time resolution.

4.7 DATA LOGGING SPECIFICATIONS

Logged Measurements Up to 16 measurement data per record (each of which can be 1 measurement or up to 500

harmonic measurements)

Internal FIFO 32Mbyte (always in binary format, 4 bytes per data)

Buffer Internal ≥2Gbyte (always in binary format, 4 bytes per data) non-volatile Typically

Memory 5Mbytes/sec maximum sustained mean write rate

External Data File Format ASCII (CSV, scientific format) or Binary

Timestamp Record number + optional date and time (1 second resolution)

Maximum File Size 4Gbyte

Maximum Records Only limited by maximum file size

Start Delay Time Zero to 99 days, 99 hours, 99 minutes, 99 seconds (1 second

resolution) 0.01% + 8ms maximum error

Run Time Manual (unrestricted period of time), or 1 second to 99 days, 99 hours, 99 minutes, 99 seconds (1

second resolution)

0.01% + 8ms maximum error

Log Interval 0.002 second, or 0.01 second to 99 hours, 99 minutes, 99.99 seconds (0.01 second

resolution) 0.01% maximum error ± 2ms non-accumulating error

